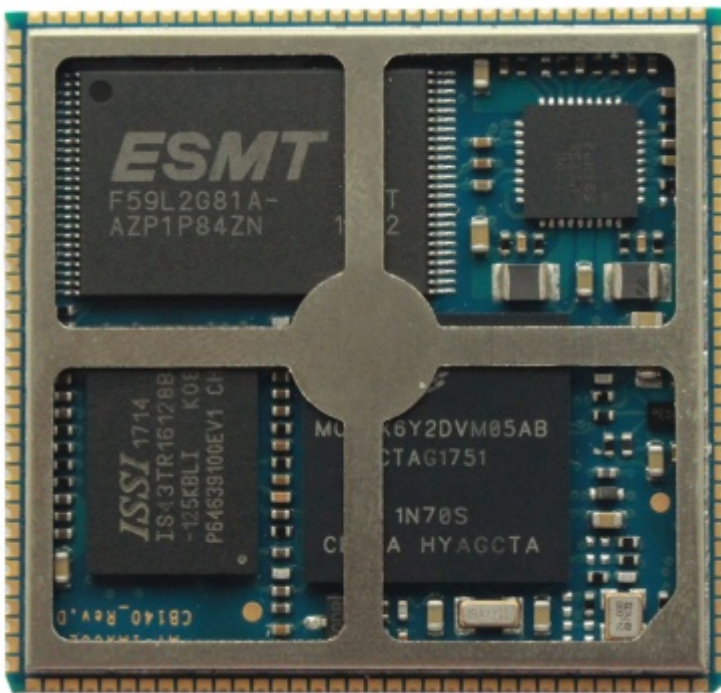


MYZR-IMX6-CB140 Hardware Introduction

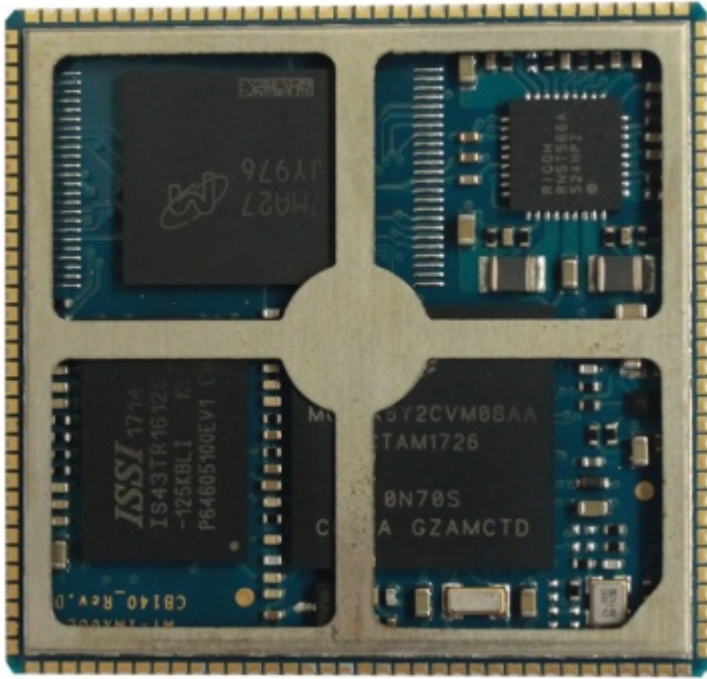
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MYZR-IMX6-CB140 View

Core board 256M-256M Front view

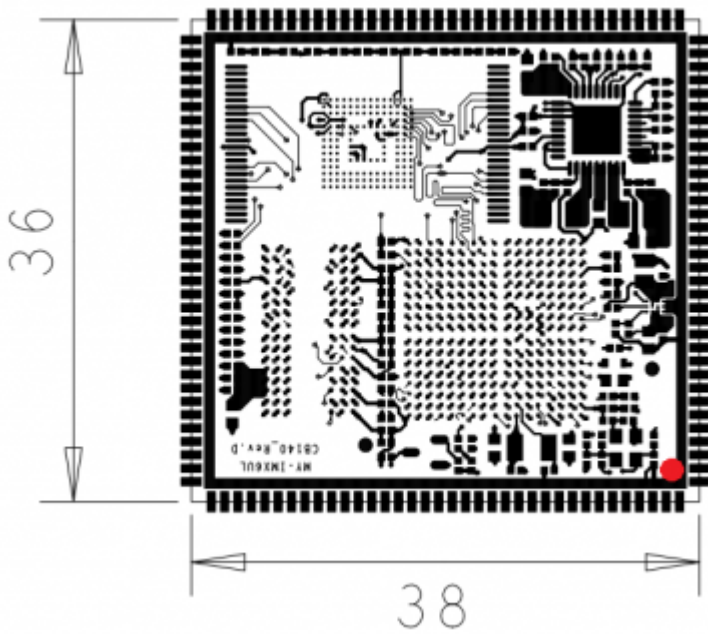


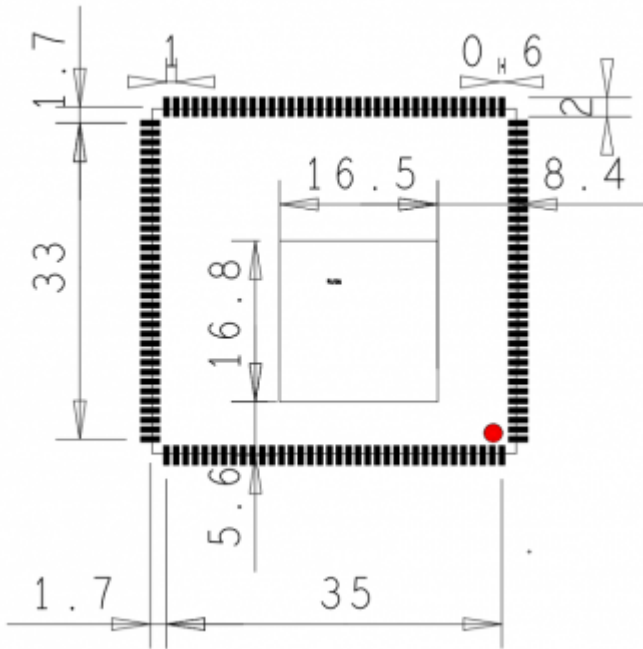
Core board 256M-4G Front view



Size

36mm * 38mm





MYZR-IMX6-CB140 parameter

Hardware configuration

CPU	i.MX 6UltraLite/i.MX6ULL	Compatible to automotive, industrial, consumer
Memory	DDR3 256MB	Scalable to 512MB
Storage	4GB eMMC	Compatible to 64GB

Power supply

5v input

Temperature range

Operating temperature

- Commercial
0°C ~ 80°C
- Industrial:
-40°C ~ 85°C
- Automotive:
-40°C ~ 105°C

Storage temperature

-60°C ~ 125°C

Operating system support

- Linux

Linux-4.1.15

Linux-4.9.88

- QT

QT-4.8.5

QT-5.5.0

Defaulted interface of hardware

Defaulted interface

CCM	CCM_CLK1	Support	External clock output
	PMIC_STBY_REQ	Support	Used for core board
ECSPI	ECSPI1	Support	1 chip select
	ECSPI2	Support	1 chip select
ENET	ENET1	Support	10/100-Mbps
FLEXCAN	FLEXCAN1	Support	
	FLEXCAN2	Support	
GPIO	GPIO1	Support	Two used for SD1,two used for ENET1
	GPIO4	Support	one used for RTC
	GPIO5	Support	nine used for universal GTIO, one used for touch
I2C	I2C1	Support	
	I2C2	Support	
eLCDIF	LCDIF	Support	24 bit
PWM	PWM2	Support	
	PWM4	Support	used for LCD backlight control
	PWM5	Support	
JTAG	SJC	Support	
SRC	ONOFF	Support	
	POR_B	Support	External re-set signal
	BOOT_MODE0	Support	Boot mode control
	BOOT_MODE1	Support	Boot mode control
UART	UART1	Support	Can support RS485
	UART2	Support	
	UART3	Support	
	UART4	support	
	UART5	support	
	UART6	support	
	UART7	support	
	UART8	support	可支持RS485
USB	USB_OTG1	support	used for USB OTG interface
	USB_OTG2	support	used for USB HOST interface
uSDHC	uSDHC1	support	4 bit
	uSHDC2	support	8bit,used for eMMC of core board
WDOG	WDOG1	support	

Pin definition & detailed functional description

Pin NO.	Pin Name	CPU Ball Name	Functional description	Pin voltage
1	CLK1_N	P16	CCM_CLK1_N	2.5V
2	CLK1_P	P17	CCM_CLK1_P	2.5V
3	WDOG_B	N17	ADC1_IN8	3.3V
			ADC2_IN8	
			CCM_PMIC_READY	
			CSI_VSYNC	
			GPIO1_IO08	
			PWM1_OUT	
			SPDIF_OUT	
			UART5_RTS_B	
			USDHC2_VSELECT	
			WDOG1_WDOG_B	
4	PWM4	M17	ADC1_IN5	3.3V
			ADC2_IN5	
			CSI_FIELD	
			ENET2_EVENT0_OUT	
			GPIO1_IO05	
			PWM4_OUT	
			UART5_RX_DATA	
			USB_OTG2_ID	
			USDHC1_VSELECT	
			XTALOSC_REF_CLK2	
5	PWM2	M15	ADC1_IN9	3.3V
			ADC2_IN9	
			CSI_HSYNC	
			GPIO1_IO09	
			PWM2_OUT	
			SPDIF_IN	
			UART5_CTS_B	
			USDHC1_RESET_B	
			USDHC2_RESET_B	
			WDOG1_WDOG_ANY	
6	PWM5	E6	CSI_FIELD	3.3V
			EIM_WAIT	
			GPIO4_IO16	

			NAND_DQS	
			PWM5_OUT	
			QSPI_A_SS0_B	
			SDMA_EXT_EVENT1	
			SPDIF_EXT_CLK	
7	USB_OTG2_PWR	L14	ADC1_IN2	3.3V
			ADC2_IN2	
			GPIO1_IO02	
			GPT1_COMPARE2	
			I2C1_SCL	
			SDMA_EXT_EVENT0	
			UART1_TX_DATA	
			USB_OTG2_PWR	
			USDHC1_WP	
			XTALOSC_ENET_REF_CLK_25M	
8	USB_OTG1_OC	L15	ADC1_IN1	3.3V
			ADC2_IN1	
			ENET1_EVENT0_OUT	
			GPIO1_IO01	
			GPT1_COMPARE1	
			I2C2_SDA	
			MQS_LEFT	
			USB_OTG1_OC	
			WDOG1_WDOG_B	
			XTALOSC_REF_CLK2	
9	USB_OTG1_PWR	M16	ADC1_IN4	3.3V
			ADC2_IN4	
			ENET2_EVENT0_IN	
			GPIO1_IO04	
			PWM3_OUT	
			UART5_TX_DATA	
			USB_OTG1_PWR	
			USDHC1_RESET_B	
			XTALOSC_REF_CLK1	
10	USB_OTG2_OC	L17	ADC1_IN3	3.3V
			ADC2_IN3	
			GPIO1_IO3	

			GPT1_COMPARE3	
			I2C1_SDA	
			UART1_RX_DATA	
			USB_OTG2_OC	
			USDHC1_CD_B	
11	CAN2_RX	H14	CSI_DATA9	3.3V
			ECSPI3_MISO	
			ENET1_COL	
			FLEXCAN2_RX	
			GPIO1_IO23	
			GPT1_COMPARE3	
			UART2_RTS_B	
12	CAN2_TX	J15	CSI_DATA8	3.3V
			ECSPI3_MOSI	
			ENET1_CRIS	
			FLEXCAN2_TX	
			GPIO1_IO22	
			GPT1_COMPARE2	
			SJC_DE_B	
			UART2_CTS_B	
13	CAN1_RX	G14	CSI_DATA11	3.3V
			ENET1_EVENT1_OUT	
			ENET2_TX_ER	
			FLEXCAN1_RX	
			GPIO1_IO27	
			UART3_RTS_B	
			WDOG1_WDOG_B	
14	CAN1_TX	H15	CSI_DATA9	3.3V
			ENET1_EVENT1_IN	
			ENET2_RX_CLK	
			EPIT2_OUT	
			FLEXCAN1_TX	
			GPIO1_IO26	
			UART3_CTS_B	
15	ENET_MDC	L16	ADC1_IN7	3.3V
			ADC2_IN7	
			ENET1_MDC	

			ENET2_MDC	
			CSI_PIXCLK	
			GPIO1_IO07	
			UART1_RTS_B	
			USDHC2_CD_B	
16	ENET_MDIO	K17	ADC1_IN6	3.3V
			ADC2_IN6	
			CCM_REF_EN_8	
			CSI_MCLK	
			ENET1_MDIO	
			ENET2_MDIO	
			GPIO1_IO06	
			UART1_CTS_B	
			USDHC2_WP	
17	ENET1_TXD1	E14	CSI_DATA20	3.3V
			ENET1_TDATA1	
			ENET2_MDIO	
			GPIO2_IO4	
			KPP_ROW2	
			PWM5_OUT	
			UART6_CTS_B	
			WDOG1_WDOG_RST_B_DEB	
18	ENET1_TXEN	F15	CSI_DATA21	3.3V
			ENET1_TX_EN	
			ENET2_MDC	
			GPIO2_IO5	
			KPP_COL2	
			PWM6_OUT	
			UART6_RTS_B	
			WDOG2_WDOG_RST_B_DEB	
19	ENET1_RXD0	F16	CSI_DATA16	3.3V
			ENET1_RDATA0	
			FLEXCAN1_TX	
			GPIO2_IO0	
			KPP_ROW0	
			PWM1_OUT	
			UART4_RTS_B	

			USDHC1_LCTL	
20	ENET1_TXD0	E15	CSI_DATA19	3.3V
			ENET1_TDATA0	
			FLEXCAN2_RX	
			GPIO2_IO3	
			KPP_COL1	
			UART5_CTS_B	
			USDHC2_VSELECT	
21	ENET1_RXD1	E17	CSI_DATA17	3.3V
			ENET1_RDATA1	
			FLEXCAN1_RX	
			GPIO2_IO1	
			KPP_COL0	
			PWM2_OUT	
			UART4_CTS_B	
USDHC2_LCTL				
22	ENET1_TX_CLK	F14	CSI_DATA22	3.3V
			ENET1_TX_CLK	
			GPIO2_IO6	
			GPT1_CLK	
			KPP_ROW3	
			PWM7_OUT	
			UART7_CTS_B	
XTALOSC_REF_CLK1				
23	ENET1_CRS_DV	E16	CSI_DATA18	3.3V
			ENET1_RX_EN	
			FLEXCAN2_TX	
			GPIO2_IO2	
			KPP_ROW1	
			UART5_RTS_B	
24	ENET1_RXER	D15	USDHC1_VSELECT	3.3V
			CSI_DATA23	
			EIM_CRE	
			ENET1_RX_ER	
			GPIO2_IO7	
			GPT1_CAPTURE2	
			KPP_COL3	

			PWM8_OUT	
			UART7_RTS_B	
25	UART1_RXD	K16	CSI_DATA3	3.3V
			ENET1_RDATA3	
			GPIO1_IO17	
			GPT1_CLK	
			I2C3_SDA	
			SPDIF_IN	
			UART1_RX_DATA	
26	UART1_CTS	K15	CSI_DATA4	3.3V
			ENET1_RX_CLK	
			ENET2_1588_EVENT1_IN	
			GPIO1_IO18	
			UART1_CTS_B	
			USDHC1_WP	
			USDHC2_WP	
27	UART1_TXD	K14	CSI_DATA2	3.3V
			ENET1_RDATA2	
			GPIO1_IO16	
			GPT1_COMPARE1	
			I2C3_SCL	
			SPDIF_OUT	
			UART1_TX_DATA	
28	UART1_RTS	J14	CSI_DATA5	3.3V
			ENET1_TX_ER	
			ENET2_1588_EVENT1_OUT	
			GPIO1_IO19	
			UART1_RTS_B	
			USDHC1_CD_B	
			USDHC2_CD_B	
29	UART2_TXD	J17	CSI_DATA6	3.3V
			ECSPI3_SS0	
			ENET1_TDATA2	
			GPIO1_IO20	
			GPT1_CAPTURE1	
			I2C4_SCL	
			UART2_TX_DATA	

30	UART2_RXD	J16	CSI_DATA7	3.3V
			ECSPI3_SCLK	
			ENET1_TDATA3	
			GPIO1_IO21	
			GPT1_CAPTURE2	
			I2C4_SDA	
			UART2_RX_DATA	
31	UART3_TXD	H17	CSI_DATA1	3.3V
			ENET2_RDATA2	
			GPIO1_IO24	
			SIM1_PORT0_PD	
			UART2_CTS_B	
			UART3_TX_DATA	
			USB_OTG1_ID	
32	UART3_RXD	H16	CSI_DATA0	3.3V
			ENET2_RDATA3	
			EPIT1_OUT	
			GPIO1_IO25	
			SIM2_PORT0_PD	
			UART2_RTS_B	
			UART3_RX_DATA	
33	UART4_TXD	G17	CSI_DATA12	3.3V
			ECSPI2_SCLK	
			ENET2_TDATA2	
			GPIO1_IO28	
			I2C1_SCL	
			UART4_TX_DATA	
34	UART4_RXD	G16	CSI_DATA13	3.3V
			ECSPI2_SS0	
			ENET2_TDATA3	
			GPIO1_IO29	
			I2C1_SDA	
			UART4_RX_DATA	
35	UART5_TXD	F17	CSI_DATA14	3.3V
			ECSPI2_MOSI	
			ENET2_CRS	
			GPIO1_IO30	

			I2C2_SCL	
			UART5_TX_DATA	
36	UART5_RXD	G13	CSI_DATA15	3.3V
			ECSPI2_MISO	
			ENET2_COL	
			GPIO1_IO31	
			I2C2_SDA	
			UART5_RX_DATA	
37	UART8_RTS	D16	ECSPI4_SS0	3.3V
			EIM_ADDR25	
			ENET2_RX_ER	
			GPIO2_IO15	
			KPP_COL7	
			SIM2_PORT0_SVEN	
			UART8_RTS_B	
			WDOG1_WDOG_ANY	
38	UART6_RXD	C16	ENET1_MDC	3.3V
			ENET2_RDATA1	
			GPIO2_IO9	
			I2C3_SDA	
			KPP_COL4	
			SIM1_PORT0_CLK	
			UART6_RX_DATA	
			USB_OTG1_OC	
39	UART8_CTS	D17	ECSPI4_MISO	3.3V
			ENET2_TX_CLK	
			GPIO2_IO14	
			KPP_ROW7	
			SIM2_PORT0_RST_B	
			UART8_CTS_B	
			USB_OTG2_ID	
			XTALOSC_REF_CLK2	
40	UART6_TXD	C17	ENET1_MDIO	3.3V
			ENET2_RDATA0	
			GPIO2_IO8	
			I2C3_SCL	
			KPP_ROW4	

			SIM1_PORT0_TRXD	
			UART6_TX_DATA	
			USB_OTG1_PWR	
41	UART8_RXD	B15	ECSPI4_MOSI	3.3V
			EIM_ACLK_FREERUN	
			ENET2_TX_EN	
			GPIO2_IO13	
			KPP_COL6	
			SIM2_PORT0_CLK	
			UART8_RX_DATA	
			USB_OTG2_OC	
42	UART7_TXD	B17	EIM_ADDR26	3.3V
			ENET2_RX_EN	
			GPIO2_IO10	
			I2C4_SCL	
			KPP_ROW5	
			SIM1_PORT0_RST_B	
			UART7_TX_DATA	
			XTALOSC_ENET_REF_CLK_25M	
43	UART8_TXD	A16	ECSPI4_SCLK	3.3V
			EIM_EB3_B	
			ENET2_TDATA1	
			GPIO2_IO12	
			KPP_ROW6	
			SIM2_PORT0_TRXD	
			UART8_TX_DATA	
			USB_OTG2_PWR	
44	UART7_RXD	A15	EIM_EB2_B	3.3V
			ENET2_TDATA0	
			GPIO2_IO11	
			I2C4_SDA	
			KPP_COL5	
			SIM1_PORT0_SVEN	
			UART7_RX_DATA	
			45	
46	5VIN	---	---	5V (input)
47				

48				
49	DCDC_3V3	---	30mA (Use for LED or Power enable)	3.3V(output)
50	VDD_SNV5_IN	---	5mA(Only use for Boot Mode)	3.3V(output)
51	GND	---	---	---
52	GND	---	---	---
53	LCD_DATA23	B16	CSI_DATA15	3.3V
			ECSPI1_MISO	
			EIM_DATA15	
			GPIO3_IO28	
			LCDIF_DATA23	
			MQS_LEFT	
			SRC_BT_CFG31	
			USDHC2_DATA3	
54	LCD_DATA19	D14	CSI_DATA11	3.3V
			EIM_DATA11	
			GPIO3_IO24	
			LCDIF_DATA19	
			PWM6_OUT	
			SRC_BT_CFG27	
			USDHC2_CLK	
			WDOG1_WDOG_ANY	
55	LCD_DATA20	C14	CSI_DATA12	3.3V
			ECSPI1_SCLK	
			EIM_DATA12	
			GPIO3_IO25	
			LCDIF_DATA20	
			SRC_BT_CFG28	
			UART8_TX_DATA	
			USDHC2_DATA0	
56	LCD_DATA21	B14	CSI_DATA13	3.3V
			ECSPI1_SS0	
			EIM_DATA13	
			GPIO3_IO26	
			LCDIF_DATA21	
			SRC_BT_CFG29	
			UART8_RX_DATA	

			USDHC2_DATA1	
57	LCD_DATA22	A14	CSI_DATA14	3.3V
			ECSPI1_MOSI	
			EIM_DATA14	
			GPIO3_IO27	
			LCDIF_DATA22	
			MQS_RIGHT	
			SRC_BT_CFG30	
			USDHC2_DATA2	
58	LCD_DATA17	B13	ARM_PLATFORM_TRACE_CTL	3.3V
			CSI_DATA0	
			EIM_DATA9	
			GPIO3_IO22	
			LCDIF_DATA17	
			SRC_BT_CFG25	
			UART7_RX_DATA	
USDHC2_DATA7				
59	LCD_DATA18	A13	ARM_PLATFORM_EVENTO	3.3V
			CSI_DATA10	
			EIM_DATA10	
			GPIO3_IO23	
			LCDIF_DATA18	
			PWM5_OUT	
			SRC_BT_CFG26	
			USDHC2_CMD	
60	LCD_DATA13	B12	ARM_PLATFORM_TRACE13	3.3V
			CSI_DATA21	
			EIM_DATA5	
			GPIO3_IO18	
			LCDIF_DATA13	
			SAI3_TX_BCLK	
			SRC_BT_CFG13	
			USDHC2_RESET_B	
61	LCD_DATA14	A12	ARM_PLATFORM_TRACE14	3.3V
			CSI_DATA22	
			EIM_DATA6	
			GPIO3_IO19	

			LCDIF_DATA14	
			SAI3_RX_DATA	
			SRC_BT_CFG14	
			USDHC2_DATA4	
62	LCD_DATA8	B11	ARM_PLATFORM_TRACE8	3.3V
			CSI_DATA16	
			EIM_DATA0	
			FLEXCAN1_TX	
			GPIO3_IO13	
			LCDIF_DATA8	
			SRC_BT_CFG8	
			SPDIF_IN	
63	LCD_DATA9	A11	ARM_PLATFORM_TRACE9	3.3V
			CSI_DATA17	
			EIM_DATA1	
			FLEXCAN1_RX	
			GPIO3_IO14	
			LCDIF_DATA9	
			SAI3_MCLK	
			SRC_BT_CFG9	
64	LCD_DATA7	D11	ARM_PLATFORM_TRACE7	3.3V
			ECSPI1_SS3	
			ENET2_1588_EVENT3_OUT	
			GPIO3_IO12	
			LCDIF_DATA7	
			SRC_BT_CFG7	
			SPDIF_EXT_CLK	
			UART7_RTS_B	
65	LCD_DATA6	A10	ARM_PLATFORM_TRACE6	3.3V
			ECSPI1_SS2	
			ENET2_1588_EVENT3_IN	
			GPIO3_IO11	
			LCDIF_DATA6	
			SRC_BT_CFG6	
			SPDIF_LOCK	
			UART7_CTS_B	
66	LCD_DATA5	B10	ARM_PLATFORM_TRACE5	3.3V

			ECSPI1_SS1	
			ENET2_1588_EVENT2_OUT	
			GPIO3_IO10	
			LCDIF_DATA5	
			SRC_BT_CFG5	
			SPDIF_OUT	
			UART8_RTS_B	
67	LCD_DATA4	C10	ARM_PLATFORM_TRACE4	3.3V
			ENET2_1588_EVENT2_IN	
			GPIO3_IO9	
			LCDIF_DATA4	
			SAI1_TX_DATA	
			SRC_BT_CFG4	
			SPDIF_SR_CLK	
			UART8_CTS_B	
68	LCD_DATA0	B9	ARM_PLATFORM_TRACE0	3.3V
			ENET1_1588_EVENT2_IN	
			GPIO3_IO5	
			I2C3_SDA	
			LCDIF_DATA0	
			PWM1_OUT	
			SAI1_MCLK	
			SRC_BT_CFG0	
69	LCD_DATA1	A9	ARM_PLATFORM_TRACE1	3.3V
			ENET1_1588_EVENT2_OUT	
			GPIO3_IO6	
			I2C3_SCL	
			LCDIF_DATA1	
			PWM2_OUT	
			SAI1_TX_SYNC	
			SRC_BT_CFG1	
70	LCD_DATA10	E12	ARM_PLATFORM_TRACE10	3.3V
			CSI_DATA18	
			EIM_DATA2	
			FLEXCAN2_TX	
			GPIO3_IO15	
			LCDIF_DATA10	

			SAI3_RX_SYNC	
			SRC_BT_CFG10	
71	LCD_DATA15	D13	ARM_PLATFORM_TRACE15	3.3V
			CSI_DATA23	
			EIM_DATA7	
			GPIO3_IO20	
			LCDIF_DATA15	
			SAI3_TX_DATA	
			SRC_BT_CFG15	
			USDHC2_DATA5	
72	LCD_DATA16	C13	ARM_PLATFORM_TRACE_CLK	3.3V
			CSI_DATA1	
			EIM_DATA8	
			GPIO3_IO21	
			LCDIF_DATA16	
			SRC_BT_CFG24	
			UART7_TX_DATA	
			USDHC2_DATA6	
73	LCD_DATA12	C12	ARM_PLATFORM_TRACE12	3.3V
			CSI_DATA20	
			ECSPI1_RDY	
			EIM_DATA4	
			GPIO3_IO17	
			LCDIF_DATA12	
			SAI3_TX_SYNC	
			SRC_BT_CFG12	
74	LCD_DATA11	D12	ARM_PLATFORM_TRACE11	3.3V
			CSI_DATA19	
			EIM_DATA3	
			FLEXCAN2_RX	
			GPIO3_IO16	
			LCDIF_DATA11	
			SAI3_RX_BCLK	
			SRC_BT_CFG11	
75	LCD_DATA3	D10	ARM_PLATFORM_TRACE3	3.3V
			ENET1_1588_EVENT3_OUT	
			GPIO3_IO8	

			I2C4_SCL	
			LCDIF_DATA3	
			PWM4_OUT	
			SAI1_RX_DATA	
			SRC_BT_CFG3	
76	LCD_DATA2	E10	ARM_PLATFORM_TRACE2	3.3V
			ENET1_1588_EVENT3_IN	
			GPIO3_IO7	
			I2C4_SDA	
			LCDIF_DATA2	
			PWM3_OUT	
			SAI1_TX_BCLK	
			SRC_BT_CFG2	
77	LCD_RESET	E9	ARM_PLATFORM_EVENT1	3.3V
			ECSPI2_SS3	
			GPIO3_IO4	
			LCDIF_CS	
			LCDIF_RESET	
			SAI3_TX_DATA	
			WDOG1_WDOG_ANY	
78	LCD_VSYNC	C9	ECSPI2_SS2	3.3V
			GPIO3_IO3	
			LCDIF_BUSY	
			LCDIF_VSYNC	
			SAI3_RX_SYNC	
			UART4_RTS_B	
			WDOG2_WDOG_B	
79	LCD_HSYNC	D9	ECSPI2_SS1	3.3V
			GPIO3_IO2	
			LCDIF_HSYNC	
			LCDIF_RS	
			SAI3_TX_BCLK	
			UART4_CTS_B	
			WDOG3_WDOG_RST_B_DEB	
80	LCD_PCLK	A8	EIM_CS2_B	3.3V
			GPIO3_IO0	
			LCDIF_CLK	

			LCDIF_WR_RWN	
			SAI3_MCLK	
			UART4_TX_DATA	
			WDOG1_WDOG_RST_B_DEB	
81	LCD_DE	B8	ECSPI2_RDY	3.3V
			EIM_CS3_B	
			GPIO3_IO1	
			LCDIF_ENABLE	
			LCDIF_RD_E	
			SAI3_TX_SYNC	
			UART4_RX_DATA	
82	GND	---	---	---
83	I2C1_SCL	E5	CSI_PIXCLK	3.3V
			EIM_OE	
			GPIO4_IO18	
			I2C1_SCL	
			NAND_CE3_B	
			SNVS_VIO_5	
			UART6_RX_DATA	
			USDHC2_WP	
84	I2C1_SDA	F5	CSI_MCLK	3.3V
			EIM_CS0_B	
			GPIO4_IO17	
			I2C1_SDA	
			NAND_CE2_B	
			SNVS_VIO_5_CTL	
			UART6_TX_DATA	
			USDHC2_CD_B	
85	NAND_CE1N	B5	ECSPI3_MOSI	3.3V
			EIM_ADDR18	
			GPIO4_IO14	
			NAND_CE1_B	
			QSPI_A_DATA2	
			UART3_CTS_B	
			USDHC1_DATA6	
86	SD1_CLK	C1	EIM_ADDR20	3.3V
			GPIO2_IO17	

			GPT2_COMPARE2	
			SAI2_MCLK	
			SPDIF_IN	
			USB_OTG1_OC	
			USDHC1_CLK	
87	SD1_DATA3	A2	CCM_CLKO2	3.3V
			EIM_ADDR24	
			FLEXCAN2_RX	
			GPIO2_IO21	
			GPT2_CAPTURE2	
			SAI2_TX_DATA	
			USB_OTG2_ID	
			USDHC1_DATA3	
88	SD1_DATA0	B3	EIM_ADDR21	3.3V
			FLEXCAN1_TX	
			GPIO2_IO18	
			GPT2_COMPARE3	
			SAI2_TX_SYNC	
			USB_OTG1_ID	
			USDHC1_DATA0	
89	SD1_DATA1	B2	EIM_ADDR22	3.3V
			FLEXCAN1_RX	
			GPIO2_IO19	
			GPT2_CLK	
			SAI2_TX_BCLK	
			USB_OTG2_PWR	
			USDHC1_DATA1	
90	SD1_DATA2	B1	CCM_CLKO1	3.3V
			EIM_ADDR23	
			FLEXCAN2_TX	
			GPIO2_IO20	
			GPT2_CAPTURE1	
			SAI2_RX_DATA	
			USB_OTG2_OC	
			USDHC1_DATA2	
91	SD1_CMD	C2	EIM_ADDR19	3.3V
			GPIO2_IO16	

			GPT2_COMPARE1	
			SAI2_RX_SYNC	
			SDMA_EXT_EVENT0	
			SPDIF_OUT	
			USB_OTG1_PWR	
			USDHC1_CMD	
92	GND	---	---	---
93	ECSPI1_MOSI	D2	CSI_DATA8	3.3V
			ECSPI1_MOSI	
			EIM_AD6	
			GPIO4_IO27	
			SAI1_RX_DATA	
			SIM2_PORT1_SVEN	
			USDHC1_RESET_B	
			USDHC2_DATA6	
94	ECSPI2_MOSI	E2	CSI_DATA4	3.3V
			ECSPI2_MOSI	
			EIM_AD2	
			GPIO4_IO23	
			SAI1_RX_SYNC	
			SIM1_PORT1_TRXD	
			UART5_RTS_B	
			USDHC2_DATA2	
95	ECSPI1_MISO	D1	CSI_DATA9	3.3V
			ECSPI1_MISO	
			EIM_AD7	
			GPIO4_IO28	
			SAI1_TX_DATA	
			SIM2_PORT1_TRXD	
			USDHC1_VSELECT	
			USDHC2_DATA7	
96	ECSPI1_SCLK	D4	CSI_DATA6	3.3V
			ECSPI1_SCLK	
			EIM_AD4	
			GPIO4_IO25	
			SAI1_TX_SYNC	
			SIM2_PORT1_CLK	

			USDHC1_WP	
			USDHC2_DATA4	
97	ECSPI2_SCLK	E4	CSI_DATA2	3.3V
			ECSPI2_SCLK	
			EIM_AD0	
			GPIO4_IO21	
			SIM1_PORT1_RST_B	
			UART5_TX_DATA	
			USDHC2_DATA0	
98	ECSPI1_CS0	D3	CSI_DATA7	3.3V
			ECSPI1_SS0	
			EIM_AD5	
			GPIO4_IO26	
			SAI1_TX_BCLK	
			SIM2_PORT1_RST_B	
			USDHC1_CD_B	
USDHC2_DATA5				
99	ECSPI2_CS0	E3	CSI_DATA3	3.3V
			ECSPI2_SS0	
			EIM_AD1	
			GPIO4_IO22	
			SAI1_MCLK	
			SIM1_PORT1_SVEN	
			UART5_RX_DATA	
USDHC2_DATA1				
100	ECSPI2_MISO	E1	CSI_DATA5	3.3V
			ECSPI2_MISO	
			EIM_AD3	
			GPIO4_IO24	
			SAI1_RX_BCLK	
			SIM2_PORT1_PD	
			UART5_CTS_B	
USDHC2_DATA3				
101	GND	---	---	---
102	I2C2_SCL	F3	CSI_HSYNC	3.3V
			EIM_LBA_B	
			GPIO4_IO2	

			I2C2_SCL	
			PWM8_OUT	
			SIM1_PORT1_PD	
			UART6_CTS_B	
			USDHC2_CMD	
103	I2C2_SDA	F2	CSI_VSYNC	3.3V
			EIM_RW	
			GPIO4_IO19	
			I2C2_SDA	
			PWM7_OUT	
			SIM1_PORT1_CLK	
			UART6_RTS_B	
			USDHC2_CLK	
104				
105	GND	---	---	---
106				
107	SNVS_TAMPER5	N8	GPIO5_IO05	3.3V
			SNVS_TAMPER5	
108	SNVS_TAMPER8	N9	GPIO5_IO08	3.3V
			SNVS_TAMPER8	
109	SNVS_TAMPER1	R9	GPIO5_IO01	3.3V
			SNVS_TAMPER1	
110	SNVS_TAMPER4	P9	GPIO5_IO04	3.3V
			SNVS_TAMPER4	
111	SNVS_TAMPER3	P10	GPIO5_IO03	3.3V
			SNVS_TAMPER3	
112	SNVS_TAMPER7	N10	GPIO5_IO07	3.3V
			SNVS_TAMPER7	
113	SNVS_TAMPER0	R10	GPIO5_IO00	3.3V
			SNVS_TAMPER0	
114	SNVS_TAMPER2	P11	GPIO5_IO02	3.3V
			SNVS_TAMPER2	
115	SNVS_TAMPER6	N11	GPIO5_IO06	3.3V
			SNVS_TAMPER6	
116	SNVS_TAMPER9	R6	GPIO5_IO09	3.3V
			SNVS_TAMPER9	
117	GND	---	---	---

118	PMIC_ON_REQ	T9	SNVS_PMIC_ON_REQ	3.3V
119	ONOFF	R8	NC	3.3V
120	POR_B	P8	RESET	3.3V
121	BOOT_MODE1	U10	SRC_BOOT_MODE1	3.3V
122	BOOT_MODE0	T10	SRC_BOOT_MODE0	3.3V
123	GND	---	---	---
124	USB_OTG2_VBUS	U12	USB_VBUS	5V(input)
125	USB_OTG2_DP	U13	USB_OTG2_DP	3.3V
126	USB_OTG2_DN	T13	USB_OTG2_DN	3.3V
127	GND	---	---	---
128	USB_OTG1_VBUS	T12	USB_VBUS	5V(input)
129	USB_OTG1_DP	U15	USB_OTG1_DP	3.3V
130	USB_OTG1_DN	T15	USB_OTG1_DN	3.3V
131	GND	---	---	---
132	NUSB_OTG_CHD	U16	NC	3.3V
133	USB_OTG1_ID	K13	ADC_ADC1_IN0	3.3V
			ADC_ADC2_IN0	
			ENET1_1588_EVENT0_IN	
			GPIO1_IO0	
			GPT1_CAPTURE1	
			I2C2_SCL	
			MQS_RIGHT	
			USB_OTG1_ID	
			WDOG3_WDOG_B	
			XTALOSC_REF_CLK1	
134	JTAG_NTRST	N14	GPIO1_IO15	3.3V
			GPT2_COMPARE3	
			PWM8_OUT	
			SAI2_TX_DATA	
135	JTAG_TMS	P14	SJC_TRSTB	3.3V
			CCM_CLKO1	
			EPIT1_OUT	
			GPIO1_IO11	
			GPT2_CAPTURE1	
			SAI2_MCLK	
			SDMA_EXT_EVENT1	
SJC_TMS				

136	JTAG_MOD	P15	CCM_PMIC_READY	3.3V
			GPIO1_IO10	
			GPT2_CLK	
			SDMA_EXT_EVENT0	
			SJC_MOD	
			SPDIF_OUT	
			XTALOSC_ENET_REF_CLK_25M	
137	JTAG_TCK	M14	GPIO1_IO14	3.3V
			GPT2_COMPARE2	
			PWM7_OUT	
			SAI2_RX_DATA	
			SIM2_POWER_FAIL	
			SJC_TCK	
138	JTAG_TDO	N15	CCM_CLKO2	3.3V
			EPIT2_OUT	
			GPIO1_IO12	
			GPT2_CAPTURE2	
			MQS_RIGHT	
			SAI2_TX_SYNC	
			SJC_TDO	
139	JTAG_TD1	N16	GPIO1_IO13	3.3V
			GPT2_COMPARE1	
			MQS_LEFT	
			PWM6_OUT	
			SAI2_TX_BCLK	
			SIM1_POWER_FAIL	
			SJC_TDI	
140	GND	---	---	---

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