



## TA2041A

# FOUR CHANNEL CLASS-T DIGITAL AUDIO AMPLIFIER USING DIGITAL POWER PROCESSING (DPP™) TECHNOLOGY

Technical Information

Revision 5.0 – September 2004

### GENERAL DESCRIPTION

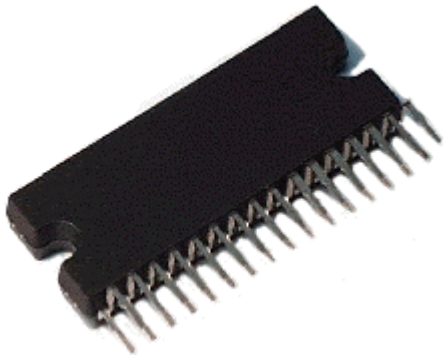
The TA2041A is a four-channel Audio Amplifier that uses Tripath's proprietary Digital Power Processing (DPP™) technology. Class-T amplifiers offer both the audio fidelity of Class-AB and the power efficiency of Class-D amplifiers. The TA2041A has been designed specifically for automotive head unit applications and is configured with four bridged outputs operating on a single 10-21V supply.

### APPLICATIONS

- Automotive Head Units
- DVD Receivers
- Multimedia Speaker Systems

### BENEFITS

- 4-channel (280W) solution – with integrated FETs - in a single 32-pin SSIP package
- High fidelity, high efficiency Class-T
- Low external component count
- Single-supply operation



### FEATURES

- Class-T architecture
- Four H-Bridge outputs
- "Audiophile" Sound Quality
- High Efficiency
- High Power @20.0V
  - 70W<sub>sat. sq. wave</sub> @ 4Ω
  - 50W @ 4Ω, 10% THD+N
- "Audiophile" Quality Sound
  - 0.04% THD+N @ 27W 4Ω
  - 0.02% IHF-IM @ 5W 4Ω
- High Efficiency
  - 86% @ 70W 4Ω
- AM "Low EMI" mode
- Pop-Free startup and shutdown
- High Dynamic Range
- Mute and Stand-By function
- Automatic DC Offset Trim
- Clip Detection Output
- Protection Modes:
  - Output Short to VPP and Ground
  - Output Short across Load
  - Load Dump Protection
  - Over-/Under-Voltage Protection
  - Over-current Protection
  - Over-temperature Protection
  - Fortuitous Open Ground

**Absolute Maximum Ratings** (Note 1)

SYMBOL	PARAMETER	Value	UNITS
V <sub>PP</sub>	Supply Voltage (V <sub>PP</sub> )	26	V
V <sub>PP</sub> <sub>MAX</sub>	Peak Supply Voltage (t <sub>≤</sub> 50ms)	60	V
V <sub>IN</sub> <sub>RANGE</sub>	Voltage Range for Input Section Pins (Note 2) Inputs (Pins 1-4, 6-9)	-0.5 to 5.5	V
T <sub>STORE</sub>	Storage Temperature Range	-55 to +150	°C
I <sub>R</sub>	Repetitive Peak Output Current	8	A
T <sub>J</sub>	Maximum Junction Temperature	150	°C
P <sub>D</sub>	Total Power Dissipation (T <sub>case</sub> = 70°C)	80	W
ESD	ESD Susceptibility - Human Body Model (Note 3)	2k	V
ESD	ESD Susceptibility – Machine Model (Note 4)	200	V

Note 1: Absolute Maximum Ratings indicate limits beyond which damage to the device may occur.

See the table below for Operating Conditions.

Note 2: The input section pins (pins 1-9, 11,12) should not be connected to voltages over 5.5V with respect to pin 10 (AGND). Please note that pins 5, 11, and 12 are outputs and can be damaged if a voltage is forced externally.

Note 3: Human body model, 100pF discharged through a 1.5K $\Omega$  resistor.

Note 4: Machine model, 220pF – 240pF discharged through all pins.

**Operating Conditions** (Note 5)

SYMBOL	PARAMETER	MIN.	TYP.	MAX.	UNITS
V <sub>PP</sub>	Supply Voltage (Note 5)	10	14.4	21	V
T <sub>A</sub>	Operating Free Air Temperature Range	-40	25	85	°C

Note 5: Recommended Operating Conditions indicate conditions for which the device is functional.

See Electrical Characteristics for guaranteed specific performance limits.

**Thermal Characteristics**

SYMBOL	PARAMETER	Value	UNITS
$\theta_{JC}$	Junction-to-case Thermal Resistance	1.0°	C/W
$\theta_{JA}$	Junction-to-ambient Thermal Resistance (still air)	20°	C/W

**Electrical Characteristics** (Note 6)

$T_A = 25^\circ\text{C}$ . Unless otherwise noted, the supply voltage is  $V_{PP}=14.4\text{V}$ . See Application/Test Circuit.

SYMBOL	PARAMETER	Conditions	MIN.	TYP.	MAX.	UNITS
$R_{IN}$	Input Impedance		43	50	57	$k\Omega$
$I_Q$	Quiescent Current	No load		220	250	mA
$I_{STBY}$	Stand-By Current	$V_{SLEEPB} < 0.15\text{V}$			0.1	mA
$V_{IL}$	Stand-By On Threshold Voltage	SLEEPB Low (amp off)			0.6	V
$V_{IH}$	Stand-By Off Threshold Voltage	SLEEPB High (amp on)	2.3			V
$V_{IL}$	Mute-On Threshold Voltage	MUTE Low			1	V
$V_{IH}$	Mute-Off Threshold Voltage	MUTE High	2.3			V
VOS	Output Offset Voltage	(Mute Off) DC trim active		$\pm 3$	$\pm 25$	mV
$V_{OH}$	Fault Reporting Logic Output High Voltage	Open Drain Output	3.5			V
$V_{OL}$	Fault Reporting Logic Output Low Voltage	$R_{FAULT} = 51k\Omega$			1	V
$V_{IH}$	AM Mode On Threshold Voltage		2.3			V
$V_{IL}$	AM Mode Off Threshold Voltage				1	V
$I_{AM}$	AM Mode Pin Input Current				1	$\mu\text{A}$

Note 6: Minimum and maximum limits are guaranteed but may not be 100% tested.

**Performance Characteristics** (Note 6)

$T_A = 25^\circ\text{C}$ . Unless otherwise noted, the supply voltage is  $V_{PP}=14.4\text{V}$ ,  $R_L = 4\Omega$ . Measurement Bandwidth = 20kHz. See Application/Test Circuit.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNITS
$P_{OUT}$	Output Power (Continuous power/ channel)	VPP=20V saturated sq. wave VPP=20V THD+N=10% VPP=20V THD+N=1% VPP=16V saturated sq. wave VPP=16V THD+N=10% VPP=16V THD+N=1% VPP=14.4V saturated sq. wave VPP=14.4V THD+N=10% VPP=14.4V THD+N=1%	65	72 50 40 49 33 26 39 27 21		W W W W W W W W W
$A_V$	Voltage Gain $V_{OUT}/V_{IN}$ , $R_{IN} = 0\Omega$		25	26	27	dB
THD + N	Total Harmonic Distortion Plus Noise	$P_{OUT} = 1\text{-}5\text{W}/\text{Channel}$ , $R_L = 4\Omega$ BW = 22Hz-20kHz(AES17)		0.04	0.10	%
IHF-IM	IHF Intermodulation Distortion	19kHz, 20kHz, 1:1 (IHF) $P_{OUT} = 1\text{W}/\text{Channel}$		0.02	0.10	%
SNR	Signal-to-Noise Ratio	A-Weighted, $P_{OUT} = 50\text{W}/\text{Channel}$	98	101		dB
PSRR	Power Supply Rejection Ratio	VPP=14.4V, Ripple=200mV, f= 1kHz		60		dB
$\eta$	Power Efficiency	$V_S=20.0\text{V}$ , 4 x 70W sat sq wave	83	86		%
CS	Channel Separation	$P_O=1\text{W}$ , f = 1kHz $P_O=1\text{W}$ , f = 10kHz		90 70		dB dB
$\epsilon_{nOUT}$	Output Noise Voltage	A-Weighted		125	150	$\mu\text{V}$

**AM Mode** (Note 6)

$T_A = 25^\circ\text{C}$ . Unless otherwise noted, the supply voltage is  $V_{PP}=14.4\text{V}$ ,  $R_L = 4\Omega$ . Measurement Bandwidth = 20kHz. See Application/Test Circuit.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNITS
$I_{OCD}$	Over-current detect		5.5			A
THD+N	Total Harmonic Dist plus Noise	$P_o=0.5\text{-}5\text{W}$ per channel		0.04%		
SNR	Signal to Noise ratio	A-Weighted, $P_o=15\text{W}$	93	96		
$P_{out}$	Output Power (Note 7)	$V_{PP}=16\text{V}$ , THD+N=10% $V_{PP}=14.4\text{V}$ , THD+N=10%	13	20 16		W W
CS	Channel Separation	$P_o=1\text{W}$ , $f=1\text{kHz}$		90		dB
$e_n$	Output Noise Voltage	A-Weighted		120	150	$\mu\text{V}$

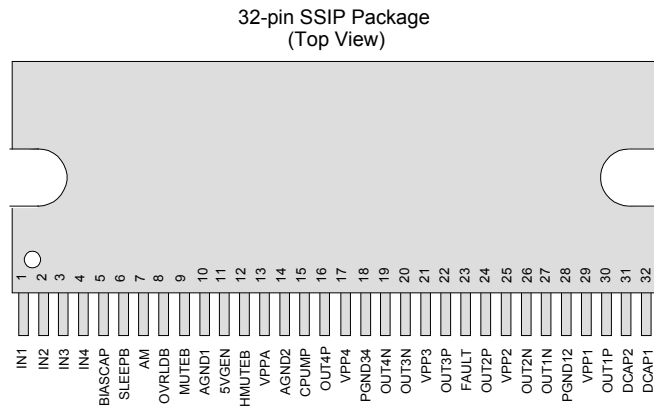
Note 7: The TA2041A heat sinking in AM Mode must be increased (as compared to Class T mode) to sustain the typical output numbers. This is due to the lower efficiency of Class B output stage operation.

**Protection Circuits** (Note 6)

$T_A = 25^\circ\text{C}$ . Unless otherwise noted, the supply voltage is  $V_{PP}=14.4\text{V}$ .

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNITS
$OV_{ON}$	Over-voltage Threshold	Over-voltage turn on (amp muted)	23.0	24.0	26.0	V
$OV_{OFF}$	Over-voltage Reset	Over-voltage turn off (mute off)	22.0	22.5		V
$UV_{OFF}$	Under-voltage Reset	Under-voltage turn off (mute off)		9.5	10.0	V
$UV_{ON}$	Under-voltage Threshold	Under-voltage turn on (amp muted)	7.8	8.1	8.6	V
$OT_{ON}$	Over-Temperature Threshold	Over-temperature turn on (amp muted)	150	160	170	$^\circ\text{C}$
$OT_{OFF}$	Over-Temperature Reset	Over-temperature turn off (mute off)	120	130	140	$^\circ\text{C}$
$I_{OC}$	Over-Current Detect	Cycle in/out of mute mode every 1s	5.5	7		A
$VP_{MAX}$	Load Dump Voltage Withstand	Test conditions, $t_r > 2.5\text{ms}$ , $t_{pulse} < 50\text{mS}$	60			V

## TA2041A Pinout

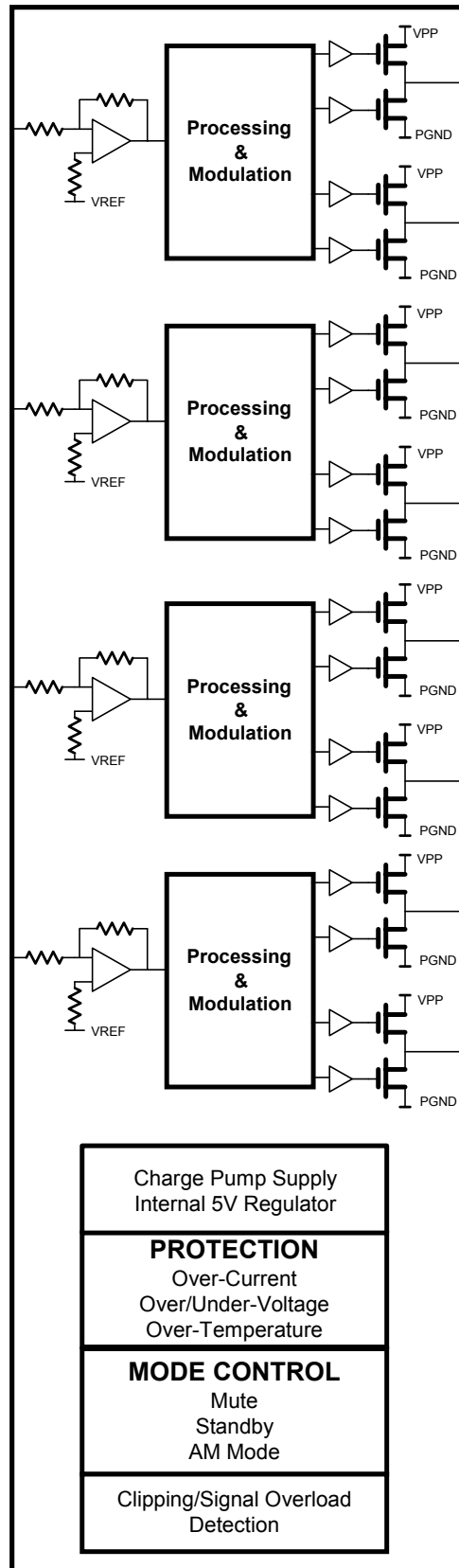


Note: The heat slug of the TA2041A is connected to PGND.

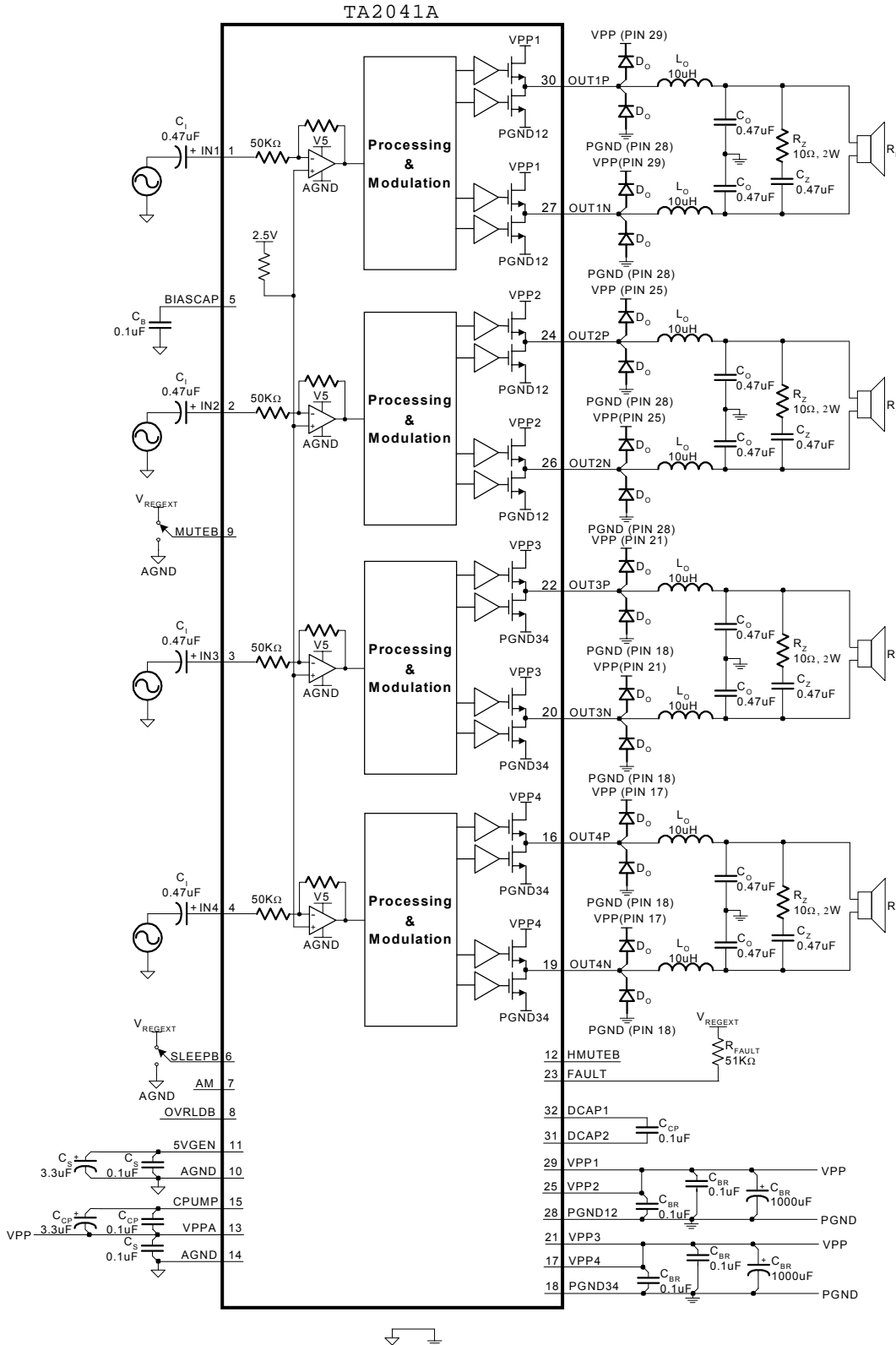
## Pin Description

PIN	NAME/FUNCTION	TYPE	DESCRIPTION
1	IN1	INPUT	Channel 1 Input
2	IN2	INPUT	Channel 2 Input
3	IN3	INPUT	Channel 3 Input
4	IN4	INPUT	Channel 4 Input
5	BIASCAP		Internal reference bypass capacitor connection
6	SLEEPB	INPUT (L)	Logic input, ACTIVE LOW. Setting SLEEP to low puts the TA2041A in sleep mode. This pin must be driven high via an external power supply or microcontroller for the TA2041A to begin operation. Input range is 0 to 5V with 3.3V compliant inputs.
7	AM	INPUT (L)	Logic input, ACTIVE HIGH. Enables Analog Mode operation. Typically driven by an external power supply of microcontroller. Input range is 0 to 5V with 3.3V compliant inputs.
8	OVRLDB	OUTPUT (L)	Logic output, ACTIVE LOW. OVRLDB low indicates the input has overloaded the amplifier
9	MUTEB	INPUT (L)	Logic Input, ACTIVE LOW. Setting MUTE to low puts the device in mute mode. Typically driven by external power supply or microcontroller. Input range is 0 to 5V with 3.3V compliant inputs.
10	AGND1	GND	Analog ground
11	5VGEN		On chip 5V regulator bypass capacitor connection
12	HMUTEB	OUTPUT (L)	Logic output, ACTIVE LOW. HMUTEB low indicates TA2041A is in mute mode
13	VPPA	POWER	Positive power supply voltage connection
14	AGND2	GND	Analog ground
15	CPUMP		Charge pump output capacitor
16	OUT4P	OUTPUT	Positive Output Channel 4
17	VPP4	POWER	Positive Supply Voltage Channel 4
18	PGND34	GND	Power Ground for Outputs 3 and 4
19	OUT4N	OUTPUT	Negative Output Channel 4
20	OUT3N	OUTPUT	Negative Output Channel 3
21	VPP3	POWER	Positive Supply Voltage Channel 3
22	OUT3P	OUTPUT	Positive Output Channel 3
23	FAULT	OUTPUT (L)	Open Drain Logic Output, ACTIVE HIGH. FAULT high indicates fault condition.
24	OUT2P	OUTPUT	Positive Output Channel 2
25	VPP2	POWER	Positive Supply Voltage Channel 2
26	OUT2N	OUTPUT	Negative Output Channel 2
27	OUT1N	OUTPUT	Negative Output Channel 1
28	PGND12	GND	Power Ground for Outputs 1 and 2
29	VPP1	POWER	Positive Supply Voltage Channel 1
30	OUT1P	OUTPUT	Positive Output Channel 1
31	DCAP2		Charge pump switching capacitor connection
32	DCAP1		Charge pump switching capacitor connection

## TA2041A Functional Block Diagram



# Application/Test Circuit



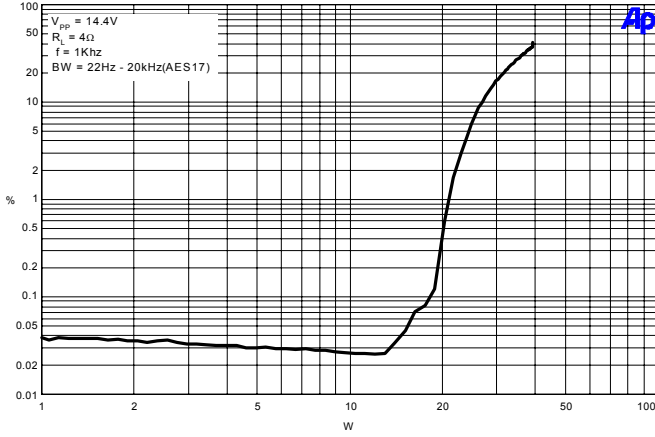


**External Components Description** (Refer to the Application/Test Circuit)

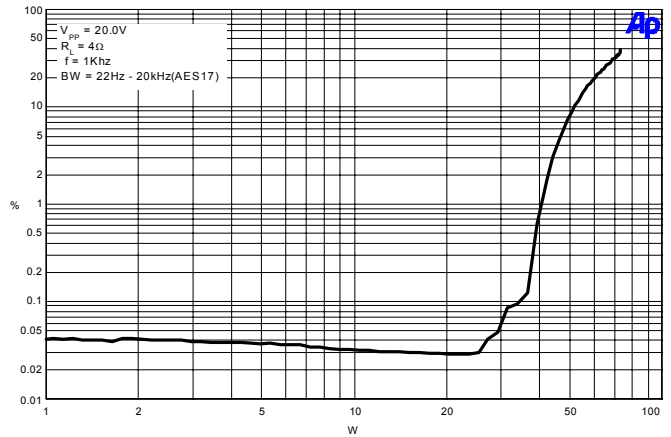
Components	Description
C <sub>I</sub>	AC input coupling capacitor which, in conjunction with R <sub>IN</sub> , forms a highpass filter at $f_c = 1/(2\pi R_{IN} C_I)$ where R <sub>IN</sub> is typically 50kΩ.
C <sub>S</sub>	Supply decoupling for the power supply pins. For optimum performance, these components should be located close to the TA2041A and returned to their respective “ground” as shown in the Application/Test Circuit.
C <sub>BR</sub>	Supply decoupling for the high current full-bridge supply pins. These components must be located as close to the power supply pins as possible to minimize output ringing which causes power supply overshoot. By reducing overshoot, these capacitors maximize the TA2041A reliability. These capacitors should have good high frequency performance including low ESR and low ESL.
C <sub>CP</sub>	Supply decoupling for the charge pump (high side gate drive supply) circuitry. These components must be located as close to the TA2041A as possible.
D <sub>O</sub>	Output diode, which is used to minimize output overshoots/undershoots on the output nodes. These devices clamp the output to the low impedance node formed by the close connection of C <sub>BR</sub> . Note the connection shown in the Application/Test Circuit. The “high side” diode protects the bottom side device from excessive BV <sub>DSS</sub> due to overshoots on the output node. The “bottom side” diode protects the top side device from excessive BV <sub>DSS</sub> due to undershoots on the output node. This device must be an ultra fast rectifier capable of sustaining the entire supply range and high peak currents.
C <sub>Z</sub>	Zobel capacitor, which in conjunction with R <sub>Z</sub> , terminates the output filter at high frequencies. Use a high quality film capacitor capable of sustaining the ripple current caused by the switching outputs.
R <sub>Z</sub>	Zobel resistor, which in conjunction with C <sub>Z</sub> , terminates the output filter at high frequencies. The combination of R <sub>Z</sub> and C <sub>Z</sub> minimizes peaking of the output filter under both no load conditions or with real world loads, including loudspeakers, which usually exhibit a rising impedance with increasing frequency. Depending on the program material, the power rating of R <sub>Z</sub> may need to be adjusted. If the system requires full power operation at 20kHz then the power rating for R <sub>Z</sub> will likely need to be increased.
L <sub>O</sub>	Output inductor, which in conjunction with C <sub>O</sub> , demodulates (filters) the switching waveform into an audio signal. Forms a second order filter with a cutoff frequency of $f_c = 1/(2\pi\sqrt{L_O C_O})$ .
C <sub>O</sub>	Output capacitor, which, in conjunction with L <sub>O</sub> , demodulates (filters) the switching waveform into an audio signal. Forms a second order low-pass filter with a cutoff frequency of $f_c = 1/(2\pi\sqrt{L_O C_O})$ . Use a high quality film capacitor capable of sustaining the ripple current caused by the switching outputs.
R <sub>FAULT</sub>	Pull-up resistor for the open drain FAULT pin output. Recommended resistor value is 51kΩ.

# Typical Performance

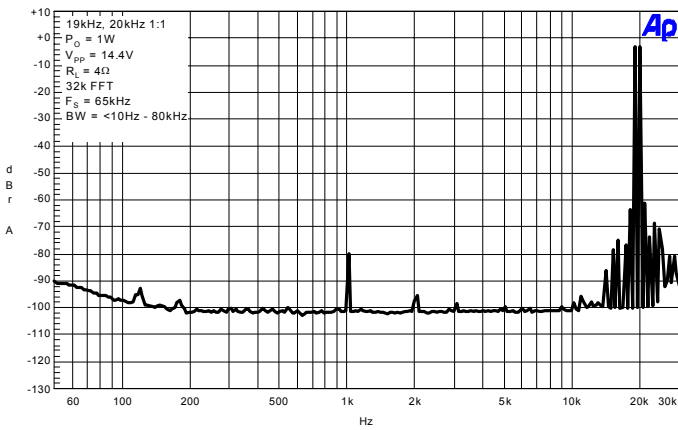
THD+N versus Output Power



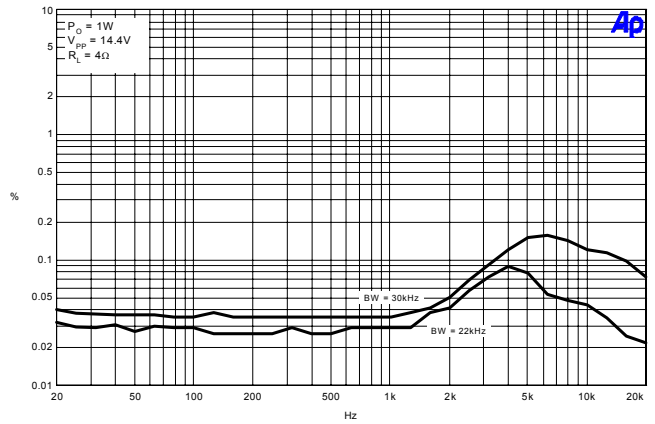
THD+N versus Output Power



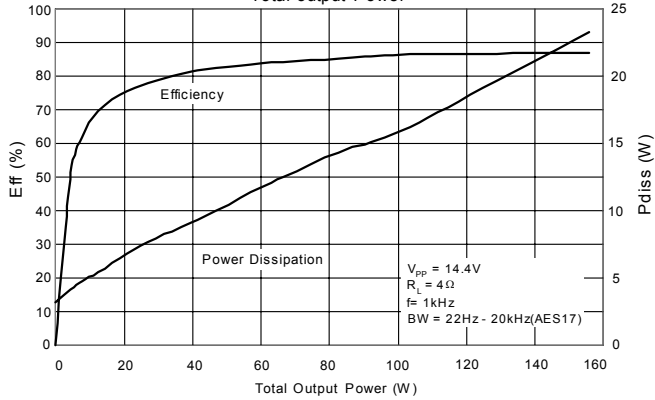
Intermodulation Distortion



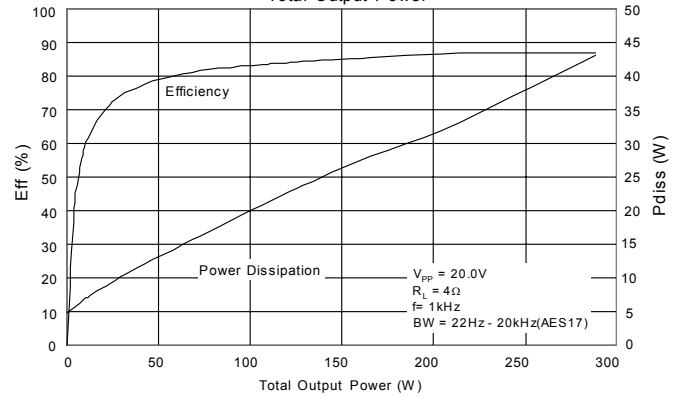
THD+N versus Frequency



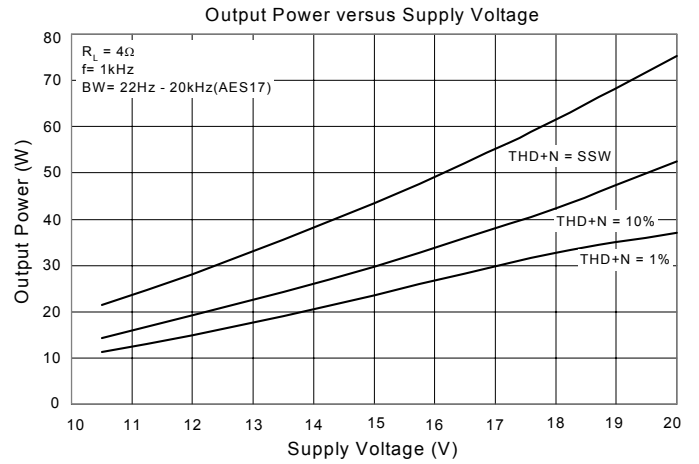
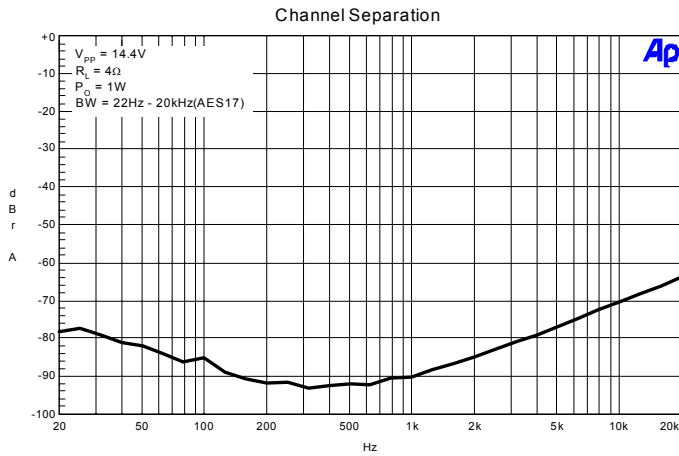
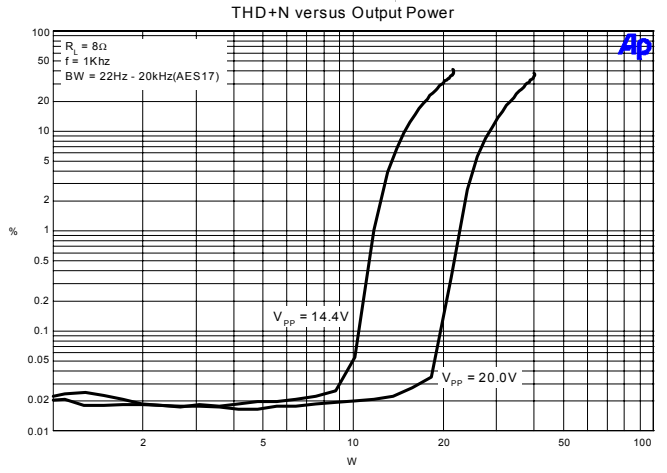
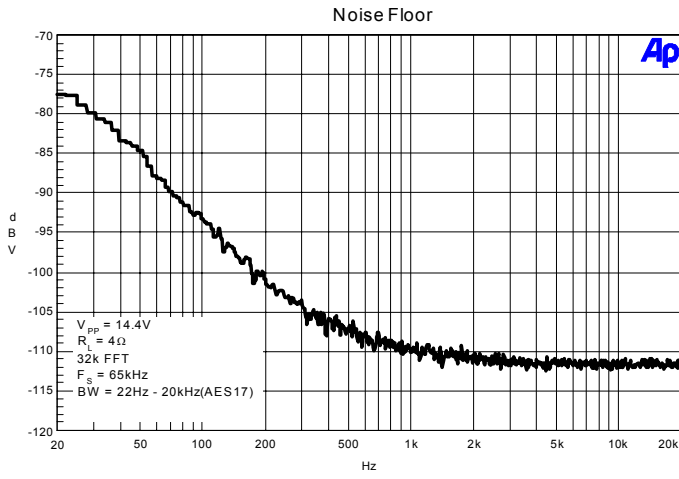
Efficiency and Power Dissipation versus Total output Power



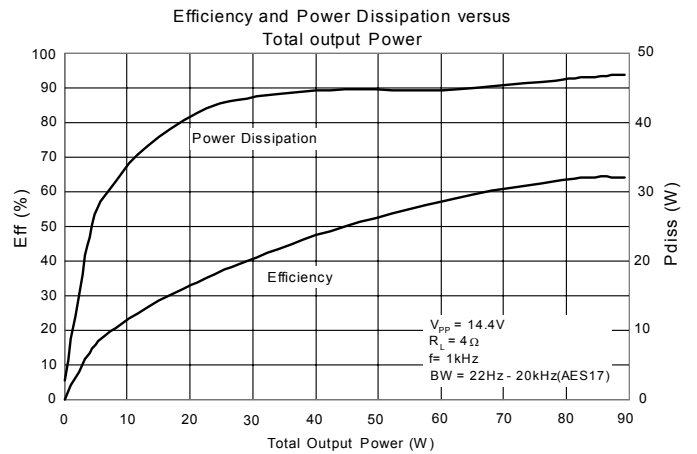
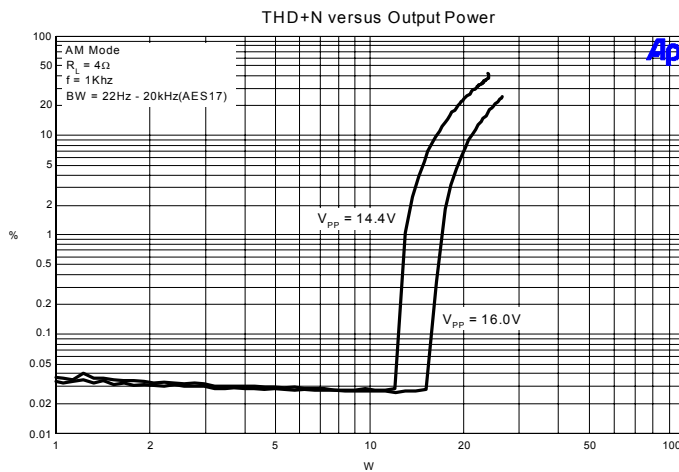
Efficiency and Power Dissipation versus Total Output Power



### Typical Performance (continued)



### Typical Performance AM Mode



## Application Information

### GENERAL DESCRIPTION

The TA2041A is a 4-channel BTL (Bridge Tied Load) output audio amplifier that operates on a single supply voltage ranging from 10-21V. The device is targeted specifically to meet the demands of OEM and aftermarket automobile in-dash head units. With a single supply voltage of 20V, the device delivers four 70 Watt (saturated square wave) channels into 4 ohm. Since the TA2041A is a switching amplifier, the average dissipation at low to medium output power is far superior to best in class AB amplifiers specifically designed for in-dash head units.

### TA2041A BASIC AMPLIFIER OPERATION

The TA2041A has three major operational blocks: the signal processor, the MOSFET driver and the power MOSFETs. The signal processor is a 5V CMOS block that amplifies the audio input signal and converts the signal to a high frequency switching pattern. This switching pattern is spread spectrum in nature and has a typical idle switching frequency of about 650kHz.

The MOSFET driver level shifts the processor's 5V switching pattern to the VPP power supply voltage and drives the power MOSFETs. The MOSFETs are N-channel devices configured as a full bridge to supply audio power to the load. The outputs of the power MOSFETs must be low pass filtered to remove the high frequency switching pattern leaving only the amplified audio signal.

### INPUT STAGE DESIGN

The TA2041A input stage is configured as an inverting amplifier that allows the system designer flexibility in setting the low frequency response. The gain is internally set at 26dB with a typical input impedance of 50kohm.

Please note that the input stage of the TA2041A runs off of an internally regulated 5V supply. Thus, the input signal should not exceed 5Vpp (absolute maximum is 6Vpp). Due to the gain structure of the TA2041A, it will likely be necessary to drive the input stage with more than 5Vpp to achieve the saturated sine wave output numbers shown in the typical characteristics. If this requirement is needed for a specific application, then the input pins (pins 1-4) should be schottky diode clamped between AGND (pin 10) and 5VGEN (pin 11). Suitable schottky diode arrays are available in small package footprints from multiple semiconductor companies.

### INPUT CAPACITOR SELECTION

$C_1$  and  $R_{IN}$  (typically 50k $\Omega$ ) determine the input low-frequency pole. Typically this pole is set at 10Hz.  $C_1$  is calculated according to:

$$C_1 = 1 / (2\pi \times F_P \times R_i)$$

where:

$$R_i = 50k\Omega$$

$$F_P = \text{Input low frequency pole (typically less than 10Hz)}$$

### AUTOMATIC OUTPUT OFFSET VOLTAGE CORRECTION

The TA2041A contains an automatic DC calibration routine that reduces the output offset voltage to a maximum of 25mV when the device is active (mute off). Please note that the DC calibration is done on the transition of MUTE<sub>B</sub> from low to high. The entire calibration sequence takes approximately 6mS after the MUTE<sub>B</sub> pin is pulled high.

## **POWER SUPPLY REQUIREMENTS**

The device is configured to operate from a single supply voltage of 10-21V. This allows the device to operate from an automobile battery under various conditions including: battery voltage with the engine off, alternator voltage with engine running and boosted voltage operation up to 21V using a DC-DC converter or voltage booster. The sleep pin must be driven from a microcontroller or external 3.3V or 5.0V power supply.

## **AM MODE**

The TA2041A is typically configured as a high power, high efficiency, four channel switching amplifier. The TA2041A also has an additional amplifier mode named "AM Mode." By pulling the AM pin to a logic high level, the TA2041A is configured as a Class B amplifier as opposed to the normal, Class T amplifier.

AM mode significantly reduces EMI generation since the output amplifiers are now operated in linear mode. Operating in Class B mode also reduces the TA2041A efficiency especially at low to medium output powers. Due to this increased power dissipation, it is recommended that the AM mode is used for applications such as AM radio playback where the average output level is minimal and a switching amplifier would most effect radio reception.

## **PROTECTION CIRCUITS**

The TA2041A is guarded against over-current, over/under voltage, and over-temperature conditions. If the device goes into one of the various protection states, the FAULT pin goes to a logic HIGH state indicating a fault condition. When this occurs, all amplifier outputs are TRI-STATED and will float to VDD.

### **OVER-CURRENT PROTECTION**

An over-current fault occurs if more than approximately 7 amps (typical) of current flows from any of the amplifier output pins. This can occur if the speaker wires are shorted together, if one side of the speaker is shorted to ground, or if an output is connected to VPP. An over-current fault sets an internal latch that automatically clears after a 600mS second delay.

### **OVER AND UNDER VOLTAGE PROTECTION**

An over-voltage fault occurs if the supply voltage is increased above 24.0 volts (typical), 23.0 volts (minimum). This fault puts the amplifier into mute and resets automatically once the supply voltage is reduced below the hysteresis band. The TA2041A also has built-in load dump protection. This circuit puts the amplifier into sleep mode if the supply voltage is increased above 30V. The TA2041A is able to survive power supply spikes to 60V if the duration is less than 50mS.

The TA2041A is also equipped with under voltage protection. This circuit is activated if the supply voltage goes below 8.1 volts (typical) and causes the output to mute. Increasing the supply voltage above the hysteresis band (typically 9.5V) will bring the amplifier out of mute mode.

### **OVER-TEMPERATURE PROTECTION**

An over-temperature FAULT occurs if the junction temperature of the part exceeds 160°C (typical). The thermal hysteresis is approximately 30°C, therefore the fault will automatically clear when the junction temperature drops below 130°C.

### **SLEEP PIN (ACTIVE LOW)**

The SLEEPB ( $\overline{\text{SLEEP}}$ ) pin is a logic input that when pulled low puts the TA2041A into a low quiescent current mode. This pin must be pulled up to an external 3.3V or 5V supply to activate (disable sleep mode) the TA2041A. The sleep pin cannot be pulled up to VPP due to internal circuitry limitations.

### **MUTEB PIN**

The MUTEB pin is a logic input that mutes the TA2041A. Pulling this pin low activates the mute circuitry. Pulling the pin high enables output switching and amplification. Please note that the input stage is still biased at approximately 2.5V, even when MUTEB pin is low. This keeps the BIASCAP,  $C_B$  and input coupling capacitors,  $C_I$ , completely charged. This allows for a clean transition from mute to on, and vice-versa, which eliminates turn-on/off pops. Please note that DC calibration is done every time MUTEB transitions from low to high. The DC calibration takes approximately 6mS.

### **FAULT PIN**

The FAULT pin is a logic output that indicates various fault conditions within the device. These conditions include: over-voltage, under-voltage, over-current at any output, low charge pump voltage, low 5V regulator voltage, and over-temperature (junction temperature greater than approximately 160°C).

The FAULT pin is an open drain output. The recommended pull-up to an external 3.3V or 5V supply is 51k $\Omega$ . Alternatively, this pin can be pulled up to VPP through a 51k $\Omega$  resistor. A logic high on this pin indicates a fault condition. This pin has a 1mA maximum sink current capability.

### **HMUTEB PIN**

The HMUTEB pin is a logic output that indicates if the TA2041A is muted. This mute state can be simply caused a low state on the MUTEB pin or by various fault conditions within the TA2041A. A logic low on the HMUTEB pin indicates the TA2041A is muted.

### **TURN-ON AND TURN-OFF NOISE**

If turn-on or turn-off noise is present in a TA2041A amplifier, the cause is frequently due to other circuitry external to the TA2041A. The TA2041A has extremely sophisticated turn on and off pop suppression circuitry that will eliminate "pops" in nearly all configurations. It is recommended that the MUTEB pin is pulled low during power-up and power-down of the VPP supply.

### **CIRCUIT BOARD LAYOUT**

The TA2041A is a power (high current) amplifier that operates at relatively high switching frequencies. Therefore, amplifier outputs switch between the supply voltage and ground at high speeds while driving high currents. This high-frequency digital signal is passed through an LC low-pass filter to recover the amplified audio signal. Since the amplifier must drive the inductive LC output filter and speaker loads, the amplifier outputs can be pulled above the supply voltage and below ground by the energy in the output inductance. To avoid subjecting the TA2041A to potentially damaging voltage stress, it is critical to have a good printed circuit board layout. It is recommended that Tripath's layout and application circuit be used for all applications and only be deviated from after careful analysis of the effects of any changes. Please refer to the TA2041A evaluation board document, EB-TA2041A, available on the Tripath website, at [www.tripath.com](http://www.tripath.com).

The following components are important to place near either their associated TA2041A pins. The recommendations are ranked in order of layout importance, either for proper device operation or performance considerations.

- The capacitors,  $C_{BR}$ , provide high frequency bypassing of the amplifier power supplies and will serve to reduce spikes and modulation of the power supply rails. Please note that bypassing requires a combination of capacitors for adequate stabilization.
- The output diodes,  $D_O$ , are used to minimize overshoots/undershoots on the output node. Improper routing of these diodes will render them useless due to PCB trace inductance. Thus, these components must be located very close to the output pins with the “other side of the diode” routed directly to the appropriate VPP or PGND pin.
- The capacitors,  $C_S$ , provide high frequency bypassing of the amplifier power supplies. Please note that bypassing requires a combination of capacitors for adequate stabilization.

### **TA2041A GROUNDING**

Proper grounding techniques are required to maximize TA2041A functionality and performance. Parametric parameters such as THD+N, Noise Floor and Crosstalk can be adversely affected if proper grounding techniques are not implemented on the PCB layout. The following discussion highlights some recommendations about grounding both with respect to the TA2041A as well as general “audio system” design rules.

The TA2041A is divided into two sections: the input section, which spans pins 1-11 and the output (high voltage) section, which spans pins 12 through pin 32. On the TA2041A evaluation board, the ground is also divided into distinct sections, one for the input and one for the output. To minimize ground loops and keep the audio noise floor as low as possible, the input and output ground should be externally connected at a single point as close to the TA2041A as possible. Additionally, any external input circuitry such as preamps, or active filters, should be referenced to pin 10.

### **OUTPUT FILTER DESIGN**

One advantage of Tripath amplifiers over PWM solutions is the ability to use higher-cutoff-frequency filters. This means any load-dependent peaking/droop in the 20kHz audio band potentially caused by the filter can be made negligible. This is especially important for applications where the user may select a 4Ω or 8Ω speaker. Furthermore, speakers are not purely resistive loads and the impedance they present changes over frequency and from speaker model to speaker model.

The core material of the output filter inductor has an effect on the distortion levels produced by a TA2041A amplifier. Tripath recommends low-mu type-2 iron powder cores because of their low loss and high linearity or high current capability bobbin types that will not saturate at peak currents below 6A.

Recently, there have been a number of dual inductors designed specifically for bridged output switching amplifiers such as the TA2041A. These dual inductors are two inductors shielded by a common ferrite shield. They may be manufactured as common mode chokes with the windings wound in the same direction or as differential mode chokes with the windings wound in opposite directions. Since the ferrite reduces the energy storage capability of the inductor, it is important to ensure that the shielded dual inductor does not saturate at the maximum currents attainable by the TA2041A. Dual inductors wound as common mode inductors may aid in reducing common mode noise to the load. They also may result in lower than initial inductances due to electric field cancellation effects. Tripath has qualified two shielded dual inductors, Toko DA1210W-1001, Sumida CDEPI106-100, and Sagami 7G09 and 7H10 series dual inductors.

Tripath also recommends that an RC damper be used after the LC low-pass filter. No-load operation of a TA2041A amplifier can create significant peaking in the LC filter, which produces strong resonant currents

that can overheat the integrated MOSFETs and/or other components. The RC dampens the peaking and prevents problems.

It is highly recommended that the design process for a TA2041A amplifier include an analysis of the interaction of intended speaker(s) with the LC filter and RC damper to ensure the desired frequency response is attained. Component values for the LC filter and RC damper may need to be altered from the Tripath suggestions to achieve the required response.

## **PERFORMANCE MEASUREMENTS OF A TA2041A AMPLIFIER**

Tripath amplifiers operate by modulating the input signal with a high-frequency switching pattern. This signal is sent through a low-pass filter (external to the TA2041A) that demodulates it to recover an amplified version of the audio input. The frequency of the switching pattern is spread spectrum and typically varies between 200kHz and 1.5MHz, which is well above the 20Hz – 22kHz audio band. The pattern itself does not alter or distort the audio input signal but it does introduce some inaudible noise components.

The measurements of certain performance parameters, particularly those that have anything to do with noise, like THD+N, are significantly affected by the design of the low-pass filter used on the output of the TA2041A and also the bandwidth setting of the measurement instrument used. Unless the filter has a very sharp roll-off just past the audio band or the bandwidth of the measurement instrument ends there, some of the inaudible noise components introduced by the Tripath amplifier switching pattern will get integrated into the measurement, degrading it.

Tripath amplifiers do not require large multi-pole filters to achieve excellent performance in listening tests, usually a more critical factor than performance measurements. Though using a multi-pole filter may remove high-frequency noise and improve THD+N type measurements (when they are made with wide-bandwidth measuring equipment), these same filters can increase distortion due to inductor non-linearity. Multi-pole filters require relatively large inductors, and inductor non-linearity increases with inductor value.





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