

DOT MATRIX LIQUID CRYSTAL DISPLAY MODULE

USER' MANUAL

MODEL: HTM12864K

PROPO	SED BY	APPROVED
Design	Approved	

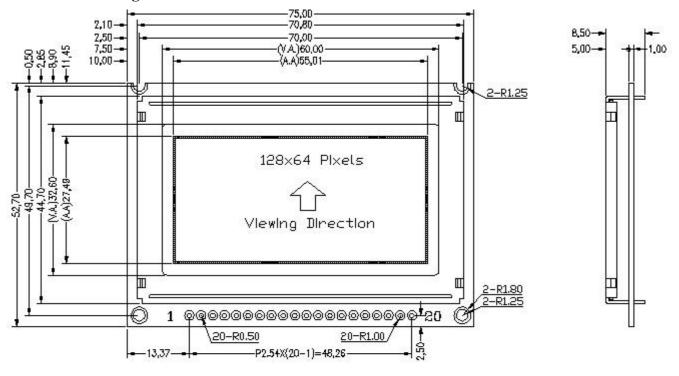
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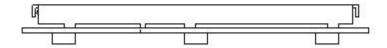
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1. Mechanical Specification

ITEM	STAN	STANDARD VALUE								
DOT MATRIX FORMAT	128									
MODULE DIMENSION	75.0 (W) X	mm								
VIEWING DISPLAY AREA	60.0 (mm								
ACTIVE DISPLAY AREA	55.01 (mm								
DOT SIZE	0.43 (mm								
DOT PITCH	0.40 (W) X 0.40	(H)	mm						
LCD TYPE	STN-Gra	y\Positive\Tı	ransflective							
LED Backlight Color		YG								
LED Backlight Input	DC +5.0V	V	45	mA						
BACKLIGHT Half-Lift TIME	50,000									

2. Mechanical Diagram

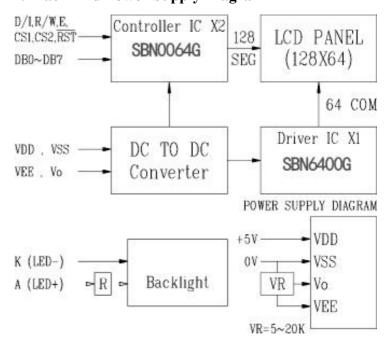




3. Interface Pin Connections

NO	SYMBOL	FUNCTION
1	VDD	DC +5V
2	GND	GND (0V)
3	VO	Contrast Adjust
4	DB0	Data Bit 0
5	DB1	Data Bit 1
6	DB2	Data Bit 2
7	DB3	Data Bit 3
8	DB4	Data Bit 4
9	DB5	Data Bit 5
10	DB6	Data Bit 6
11	DB7	Data Bit 7
12	CS1	Chip 1 enable signal
13	CS2	Chip 2 enable signal
14	/RST	Reset signal
15	R/W	Read / Write
16	RS	Data/command
17	Е	Enable signal
18	VEE	Negative voltage output
19	A+	LED+(Backlight)
20	K-	LED-(Backlight)

4. Black And Power Supply Diagram



5. Electrical Absolute Maximum Ratings

ITEM	SYMBOL	MIN.	TYPE	MAX.	UNIT
SUPPLY VOLTAGE FOR LOGIC	VDD-VSS		5.0	6.5	V
SUPPLY VOLTAGE FOR LCD	VDD-VEE			10.0	V
INPUT VOLAGE	VI	VSS		VDD	V
OPERATING TEMP.	TO	-20		+70	°C
STORAGE TEMP.	TS	-30		+80	°C

6. Environmental absolute maximum ratings

ITEM	OPER.	ATING	STOI	RAGE	REMARKS	
	MIN. MAX.		MIN.	MAX.		
Ambient Temperature	0/-20 °C 50/70 °C		-10/-30 °C	60/80 °C	NOTE 1	
Humidity	NO	ΓE 1	NO'	ГЕ 2	Without Condensation	
Vibration		4.9m/s^2		19.6m/s ²	XYZ Directions	
Shock	29.4m/s ²			490.0m/s ²	XYZ Directions	

Remarks:

NOTE (1): Ta at 60 ?C: 50 HR Max. NOTE (2): Ta < 40 ?C: 95% RH Max.

 $T_a > 40 \ ?C$: Absolute humidity must be lower than the humidity of 95% at 40 ?C.

7. Electrical Characteristics

ITEM	SYN	CONDITION	MIN.	TYPE	MAX.	UNIT
SUPPLY VOLTAGE FOR LOGIC	VDD-VSS	1	4.5	5.0	5.5	V
SUPPLY VOLTAGE FOR LCD	VDD-VEE	Ta= 25 °C	8.0		17.0	V
INPUT HIGH VOLTAGE	VIH	NOTE 1	0.7VDD		VDD	V
INPUT LOW VOLTAGE	VIL	NOTE 1	0		0.3VDD	V
SUPPLY CURRENT (LOGIC)	IDD	VDD=+5V,VEE=-5V			4.0	mA
SUPPLY CURRENT (LCD)	ILCD	VDD=+5V,VEE=-5V			3.0	mA

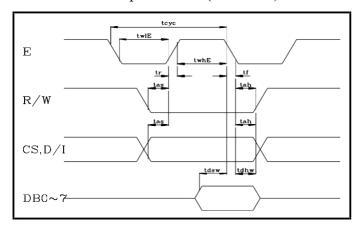
NOTE (1): CS1,CS2,R/W,D/I,DB0~7,E, and RST.

8. Electro-Optical Characteristics

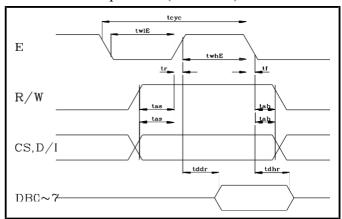
ITEM		SYM	TEMP (?C)	MIN.	TYPE	MAX.	UNIT
		tr	0/-20		2450/3264		ms
		tr	25		204		ms
RESPONSE TIM	E	tf	0/-20		600/800		ms
		tf	25		50		ms
FRAME FREQUEN	fF					Hz	
			0	12.3	13.0	13.7	V
OPERATING VOLT	AGE	Vop	25	11.0	11.7	12.4	V
			50	10.2	10.9	11.6	V
VIEWING ANGLE	F-B	Ai		55			deg
VIE WING ANGEE	R-L	Qi		60			deg
CONTRAST RAT	K	25		22.8			

9. Timing Control

Write Operation (NOTE 1)



Read Operation (NOTE 2)



Item	Symbol	Limit (Min.)	Limit (Max.)	Unit
E cycle	teye	1000		ns
E high level width	twhE	450		ns
E low level width	twlE	450		ns
E rise time	tr		25	ns
E fall time	tf		25	ns
Address set-up time	tas	140		ns
Address hold time	tah	10		ns
Data set-up time	tdsw	200		ns
Data delay time	tddr		320	ns
Data hold time (write)	tdhw	10		ns
Data hold time (read)	tdhr	20		ns

10. Instruction Set

FUNCTION	R/W	D/I	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0	FUNCTION						
1. Display ON/OFF	0	0	0	0	1	1	1	1	1	1/0	Controls the ON/OFF of the display. RAM data internal status are not affected. 1:ON, 0:OFF.						
2. Display start line	0	0	1	1		displa	y start	line (0~63)	Indicates the display data RAM displayed at the top of the screen.						
3. Set page (X address)	0	0	1	0	1	1	1	paş	page (0~7) Sets the page (X address) of RAM at the pag address) register.								
4. Set address	0	0	0	1		Υa	addres	s (0~6	53)		Sets the Y address at the Y address counter.						
5. Status Read	1	0	B U	0	ON/ OFF	R E	0	0	0	0	Reads the status. RESET 1:Reset, 0:normal.						
			S Y		011	S E T					ON/OFF 1: Display OFF, 0: Display ON. BUSY 1: In operation. 0: Ready						
6. Write Display Data	0	1			Write Data						Writes data (DB0~7) into display data RAM. After writing instruction, Y address is increased by 1 automatically.						
7. Read Display Data	1	1			Read Data Reads data (DB0~7) from display data RAM to the data bus.					Reads data (DB0~7) from display data RAM to the data bus.							

11. Description Of Instructions

(1) Display ON/OFF

	R/W	D/I	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
Code	0	0	0	0	1	1	1	1	1	D

The display data appears when D is 1 and disappers when D is 0. Though the data is not on the screen width D=0, it remains in the display data RAM. Therefore, you can make it appear by changing D=0 or D=1.

(2) Display start line

	R/W	D/I	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
Code	0	0	1	1	A	A	A	A	A	A

Z address AAAAA (binary) of the display data RAM is set at the display start line register and displayed at the top of the screen.

(3) Set page (X address)

	R/W	D/I	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
Code	0	0	1	0	1	1	1	A	A	A

X address AAA (binary) of the display data RAM is set at the X address register. After that, writing or from MPU is executed in this specified page until the next page is set.

(4) Set Y address

	R/W	D/I	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
Code	0	0	0	1	A	A	A	A	A	A

Y address AAAAA (binary) of the display RAM is set at the Y address counter is increase by every time data is written or read to or from MPU.

DB0 I DB7	PAGE 0	X=0
L		
DB0		
I	PAGE 1	X=1
DB7		
	I	
DB0		
I	PAGE 6	X=6
DB7		
DB0		
I	PAGE 7	X=7
DB7		

Address Configuration of Display RAM

(5) Status Read

	R/W	D/I	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
Code	1	0	BUSY	0	ON/OFF	RESET	0	0	0	0

BUSY: When BUSY is 1, The LSI is in internal operation. No instructions are accepted while BUSY is 1, so you should make sure that BUSY is 0 before writing the next instruction.

ON/OFF: This bit show the liquid crystal conditions ON condition or OFF condition.

When ON/OFF is 1, the display is on OFF condition. When ON/OFF is 0, the display on ON condition.

RESET: RESET=1 shows that the system is being initialized. In this condition, any instructions except Status Read instruction cannot be accepted.

RESET=0 shows that initializing has finished and system is in the usual operation.

(6) Write Display Data

	R/W	D/I	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
Code	0	1	D	D	D	D	D	D	D	D

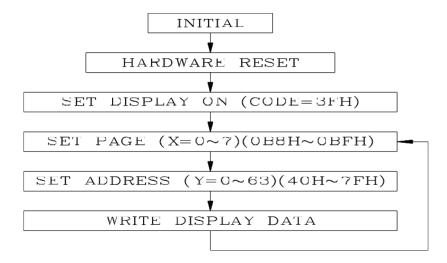
Writes 8-bit data DDDDDDDD (binary) into display data RAM. The Y address is increased by 1 automatically.

(7) Read Display Data

	R/W	D/I	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
Code	1	1	D	D	D	D	D	D	D	D

Reads out 8-bit data DDDDDDD (binary) from the display data RAM. The Y address is increased by 1 automatically. One dummy read is necessary soon after the address setting.

12. Initializing And Programming



13. Reliability Condition

13. Reliability Condition								
			TN	Гуре	STN	Type		
_			Normal Temp.	Wide Temp.	Normal Temp.	Wide Temp.		
Viewing	Horizontal Φ		±30°	±30°	±30°	±30°		
Angle	Vertical ⊕ (mm)	10°to 30°	10°to 30°	-10°to 40°	-10°to 40°		
Operatin	g Temperature		-10 to 70°C	-25 to 80°C	0 to 50°C	*-20 to 70°C		
Storage	Temperature		-20 to 80°C	-35 to 90°C	-20 to 70°C	*-30 to 80°C		
High Temper	rature (Power Of	f)	240 Hours	240 Hours	240 Hours	240 Hours		
			@70°C	@90°C	@65℃	@75℃		
Low Temper	rature (Power Off		240 Hours	240 Hours	240 Hours	240 Hours		
			@-20°C	@-35°C	@-15°C	@-25°C		
High Tempe	rature (Power On	(240 Hours	240 Hours	240 Hours	240 Hours		
			@70°C	@80°C	@60°C	@70°C		
Low Temper	rature (Power On)	240 Hours	240 Hours	240 Hours	240 Hours		
			@-10°C	@-25°C	@-10°C	@-20°C		
High Tem	perature & High		55℃/90%RH	75℃/90%RH	45℃/90%RH	65℃/90%RH		
H	Humidity			240 Hours	240 Hours	240 Hours		
Thermal Shock	<u>C</u>	A	60min@-20°C	60min@-35℃	60min@-20°C	60min@-30°C		
5 Cycle	В	В	5min@25℃	5min@25℃	5min@25℃	5min@25℃		
		С	60min@70℃	60min@90°C	60min@70°C	60min@80℃		
Exp	Expected Lift		50,000 Hours	50,000 Hours				

^{*}Wide temp. version may not available for some products, Please consult our sales engineer or respresentative.

14. Functional Test & Inspection Criteria

14.1 Sample plan

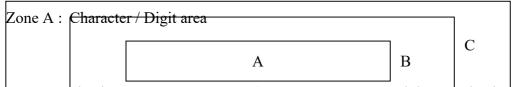
Sample plan according to MIL-STD-105D level 2, and acceptance/rejection criteria is.

Base on: Major defect: AQL 0.65 Minor defect: AQL 2.5

14.2 Inspection condition

Viewing distance for cosmetic inspection is 30cm with bare eyes, and under an environment of 800 lus (20W) light intensity. All direction for inspecting the sample should be within 45° against perpendicular line.

14.3 Definition of Inspection Zone in LCD



Zone B : Viewing area except Zone A (Zone A + Zone B = minimum) Viewing area)

Zone C: Outside viewing area (invisible area after assembly in customer's product)

Note: As a general rule, visual defects in Zone C are permissible, when it is no trouble for quality and assembly of customer's product.

14.4 Major Defect

All functional defects such as open (or missing segment), short, contrast differential, excess power consumption, smearing, leakage, etc. and overall outline dimension beyond the drawing. Are classified as major defects.

14.5 Minor Defect Except the Major defects above, all cosmetic defects are classified as minor defects.

Item No.	Item to be Inspected		Insp		Classification of			
item ivo.	item to be inspected		шър	defects				
1.	Spot defect	Zone siz	ze (mm)	Acceptable Qty			Otv	Minor
1.	(Defects in spot	20110 512	ze (mm)	A	F		C	14111101
	from)	Φ ≤	0.15		ptable		Accepta-	
	,	4 –	0.13	(cluterin			ble	
				not all	_	-		
		0.15≦ ₫	⊅ ≦ 0.20	1	1	2		
		0.20≦ ₫	⊅ ≦ 0.25	0	1	1		
		Ф>	0.25	0	()		
		Remarks:	for dark/w	hite spot, s	size (
			$\Phi = 1/2(X +$	·Y)				
2.	Line defect		Size (mm)		Acceptable Qty			Minor
	(Defects in line	L	V	V	Zc		ne	
	form)	Length	Wi	dth	A	В	С	
		Accep-	W≦	0.02	Accep-		Accep-	
		table			tał	ole	table	
		$L \leq 3.0$	W≦	€ 0.03		2		
		L>2.5	W ≤ 0.03		()		
		L ≤ 3.0	$3.0 0.03 < W \le 0$		2	2		
		L>2.5	0.03 <w< td=""><td>/ ≤ 0.05</td><td>(</td><td>)</td><td></td><td></td></w<>	/ ≤ 0.05	()		
			W>	0.05	1		d as spot	
					defect (F			
					item 14.5.1)		4.5.1)	

HTM12864K-11Y-N5P

HOTDISPL	AY				'''	11011200411-111-1101
		Remarks: The total o				
3.	Orientation defect (such as misalignment of L/C)	Not allowed inside	Minor			
4.	Polarizing	14.5.4.1 Polarizer Po 1. Shifting in Positing glass outline dir 2. Incomplete cover Shifting is not an	tion Should mension. ering of the llowed. ubble or de effector, Bu Reflector/O A A Acce	e viewing a ent on Glas	rea due to	Minor