

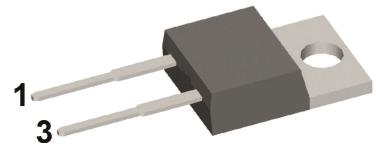
Standard Rectifier

V_{RRM} = 1200 V
 I_{FAV} = 30 A
 V_F = 1,25 V

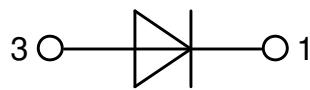
Single Diode

Part number

DSI30-12A



Backside: cathode



Features / Advantages:

- Planar passivated chips
- Very low leakage current
- Very low forward voltage drop
- Improved thermal behaviour

Applications:

- Diode for main rectification
- For single and three phase bridge configurations

Package: TO-220

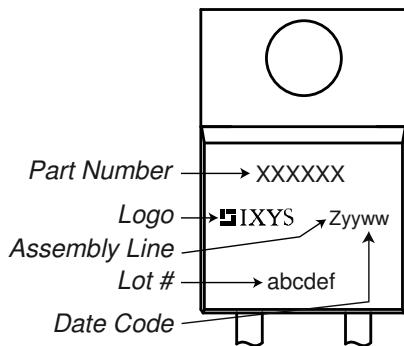
- Industry standard outline
- RoHS compliant
- Epoxy meets UL 94V-0

Rectifier

Symbol	Definition	Conditions	Ratings			
			min.	typ.	max.	
V_{RSM}	max. non-repetitive reverse blocking voltage	$T_{VJ} = 25^\circ C$			1300	V
V_{RRM}	max. repetitive reverse blocking voltage	$T_{VJ} = 25^\circ C$			1200	V
I_R	reverse current	$V_R = 1200 \text{ V}$ $V_R = 1200 \text{ V}$	$T_{VJ} = 25^\circ C$ $T_{VJ} = 150^\circ C$		40 1,5	μA mA
V_F	forward voltage drop	$I_F = 30 \text{ A}$ $I_F = 60 \text{ A}$ $I_F = 30 \text{ A}$ $I_F = 60 \text{ A}$	$T_{VJ} = 25^\circ C$ $T_{VJ} = 150^\circ C$		1,29 1,60 1,25 1,66	V V V V
I_{FAV}	average forward current	$T_C = 130^\circ C$ rectangular $d = 0.5$	$T_{VJ} = 175^\circ C$		30	A
V_{F0} r_F	threshold voltage slope resistance } for power loss calculation only		$T_{VJ} = 175^\circ C$		0,82 14,1	V $m\Omega$
R_{thJC}	thermal resistance junction to case				0,9	K/W
R_{thCH}	thermal resistance case to heatsink			0,50		K/W
P_{tot}	total power dissipation		$T_C = 25^\circ C$		160	W
I_{FSM}	max. forward surge current	$t = 10 \text{ ms}; (50 \text{ Hz}), \text{sine}$ $t = 8,3 \text{ ms}; (60 \text{ Hz}), \text{sine}$ $t = 10 \text{ ms}; (50 \text{ Hz}), \text{sine}$ $t = 8,3 \text{ ms}; (60 \text{ Hz}), \text{sine}$	$T_{VJ} = 45^\circ C$ $V_R = 0 \text{ V}$ $T_{VJ} = 150^\circ C$ $V_R = 0 \text{ V}$		300 325 255 275	A
I^2t	value for fusing	$t = 10 \text{ ms}; (50 \text{ Hz}), \text{sine}$ $t = 8,3 \text{ ms}; (60 \text{ Hz}), \text{sine}$ $t = 10 \text{ ms}; (50 \text{ Hz}), \text{sine}$ $t = 8,3 \text{ ms}; (60 \text{ Hz}), \text{sine}$	$T_{VJ} = 45^\circ C$ $V_R = 0 \text{ V}$ $T_{VJ} = 150^\circ C$ $V_R = 0 \text{ V}$		450 440 325 315	A^2s A^2s A^2s A^2s
C_J	junction capacitance	$V_R = 400 \text{ V}; f = 1 \text{ MHz}$	$T_{VJ} = 25^\circ C$	10		pF

Package TO-220

Symbol	Definition	Conditions	Ratings			
			min.	typ.	max.	Unit
I_{RMS}	<i>RMS current</i>	per terminal			35	A
T_{VJ}	<i>virtual junction temperature</i>		-40		175	°C
T_{op}	<i>operation temperature</i>		-40		150	°C
T_{stg}	<i>storage temperature</i>		-40		150	°C
Weight				2		g
M_d	<i>mounting torque</i>		0,4		0,6	Nm
F_c	<i>mounting force with clip</i>		20		60	N

Product Marking

Ordering	Ordering Number	Marking on Product	Delivery Mode	Quantity	Code No.
Standard	DSI30-12A	DSI30-12A	Tube	50	476390

Similar Part	Package	Voltage class
DSI30-08A	TO-220AC (2)	800
DSI30-08AS	TO-263AB (D2Pak) (2)	800
DSI30-08AC	ISOPLUS220AC (2)	800
DSI30-12AS	TO-263AB (D2Pak) (2)	1200
DSI30-12AC	ISOPLUS220AC (2)	1200
DSI30-16A	TO-220AC (2)	1600
DSI30-16AS	TO-263AB (D2Pak) (2)	1600

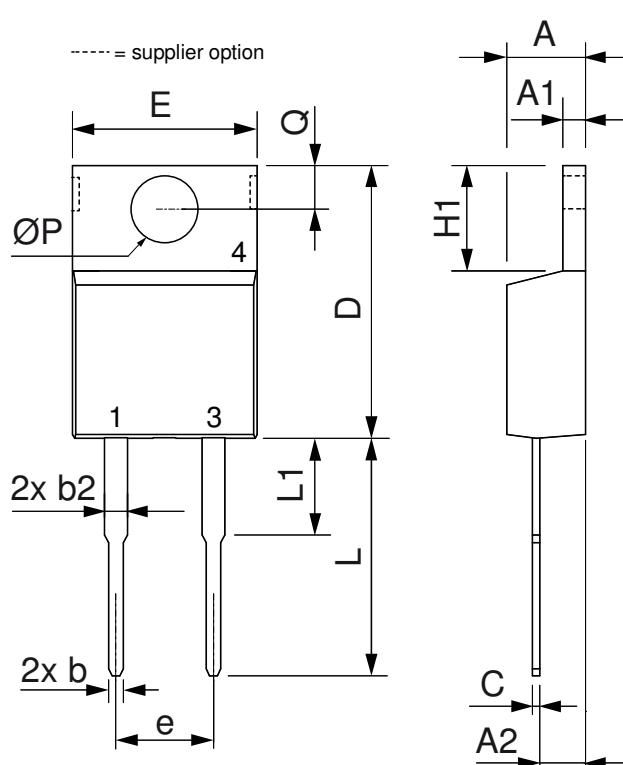
Equivalent Circuits for Simulation

* on die level

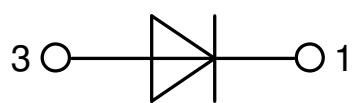
 $T_{VJ} = 175 \text{ } ^\circ\text{C}$

	Rectifier
$V_{0\max}$	threshold voltage 0,82 V
$R_{0\max}$	slope resistance * 11 mΩ

Outlines TO-220



Dim.	Millimeter		Inches	
	Min.	Max.	Min.	Max.
A	4.32	4.82	0.170	0.190
A1	1.14	1.39	0.045	0.055
A2	2.29	2.79	0.090	0.110
b	0.64	1.01	0.025	0.040
b2	1.15	1.65	0.045	0.065
C	0.35	0.56	0.014	0.022
D	14.73	16.00	0.580	0.630
E	9.91	10.66	0.390	0.420
e	5.08	BSC	0.200	BSC
H1	5.85	6.85	0.230	0.270
L	12.70	13.97	0.500	0.550
L1	2.79	5.84	0.110	0.230
ØP	3.54	4.08	0.139	0.161
Q	2.54	3.18	0.100	0.125



Rectifier

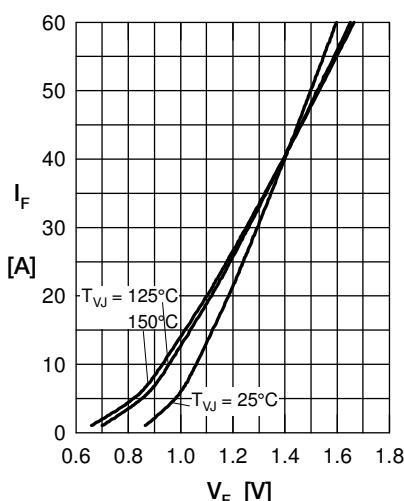


Fig. 1 Forward current versus voltage drop per diode

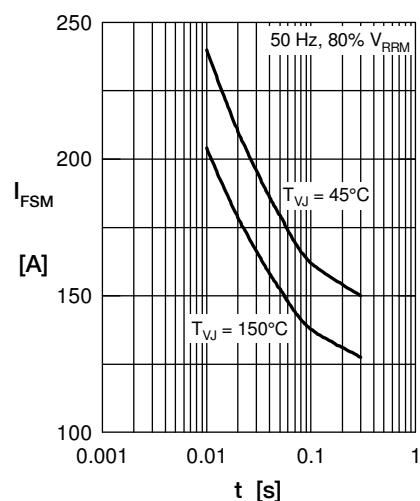


Fig. 2 Surge overload current

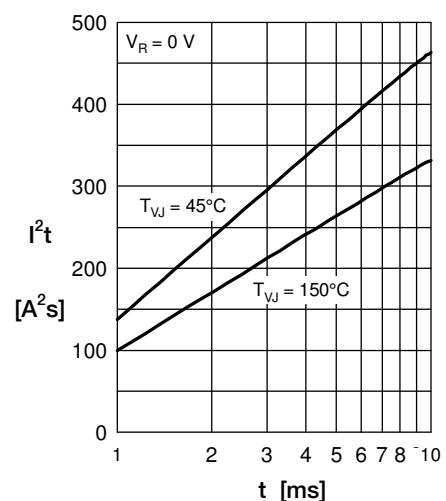
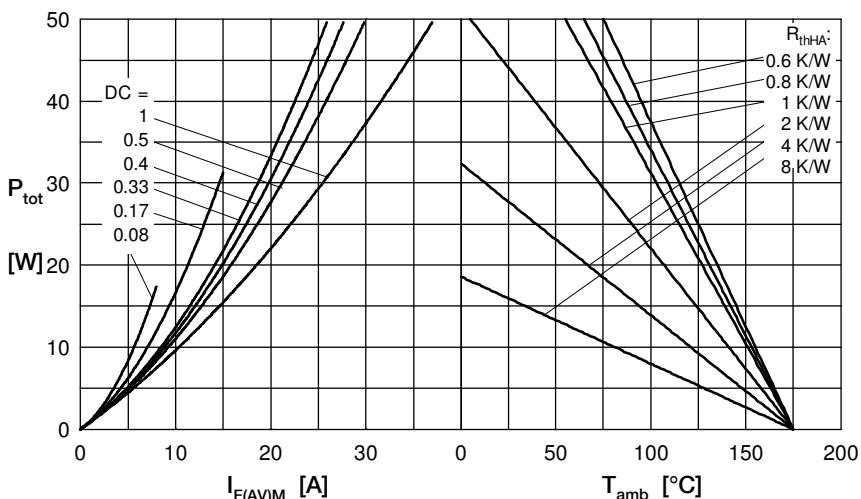
Fig. 3 I^2t versus time per diode

Fig. 4 Power dissipation vs. direct output current and ambient temperature

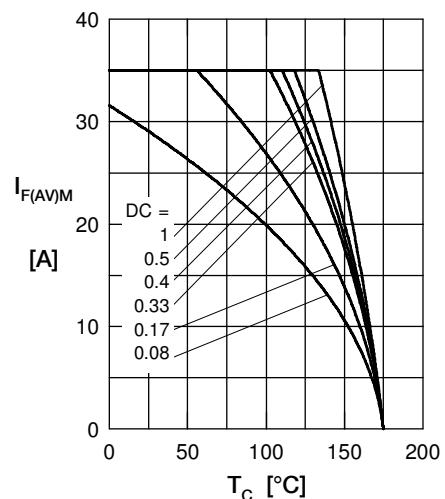


Fig. 5 Max. forward current vs. case temperature

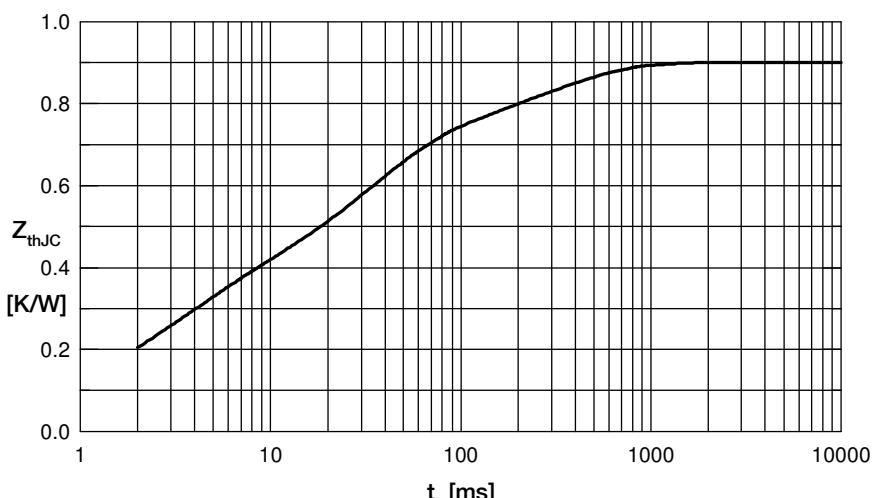


Fig. 6 Transient thermal impedance junction to case

Constants for Z_{thJC} calculation:

i	R_{thi} (K/W)	t_i (s)
1	0.03	0.0004
2	0.08	0.002
3	0.2	0.003
4	0.39	0.03
5	0.2	0.29