



MULTI-INNO TECHNOLOGY CO., LTD.

# LCD MODULE SPECIFICATION

**Model : MI12864JO**

Revision	v0.2
Engineering	
Date	
Our Reference	

# OLED

# 128X64

1.02"



## ■ PHYSICAL DATA

No.	Items:	Specification:	Unit
1	Diagonal Size	1.02	Inch
2	Resolution	128(H) x 64(V)	Lines
3	Active Area	23.02(W) x 11.86(H)	mm
4	Outline Dimension (Panel)	30.00(W) x 20.16(H)	mm
5	Pixel Pitch	0.180(W) x 0.180(H)	mm
6	Pixel Size	0.160(W) x 0.160(H)	mm
7	Driver IC	SH1101A	-
8	Grayscale	mono	-
9	Interface	Parallel / Serial	-
10	IC package type	COG	-
11	Thickness	1.5±0.1	mm
12	Weight	<2.0	g
13	Duty	1/64	-

## ■ SERIES PRODUCTS LIST

Module No.	Display Color	CIE		Luminance typical(cd/m <sup>2</sup> )	Lifetime (hrs, 25°C)	
		x	y		30% ON*	100% ON*
MI12864JO-A2	Blue-Yellow	0.16±0.05	0.27±0.05	60	80K	24K
				80	60K	18K
MI12864JO-R	Red	0.65±0.04	0.34±0.04	30	150K	45K
				50	75K	22K
MI12864JO-G	Green	0.31±0.04	0.62±0.04	80	80K	24K
				100	55K	16K
MI12864JO-Y	Yellow	0.46±0.05	0.51±0.05	60	150K	45K
				100	70K	22K
MI12864JO-W	White	0.3±0.05	0.36±0.05	60	80K	24K
				80	60K	18K

\* 30% ON:30% pixels scrolling display on;100% ON:all pixels display on

- Life Time** is defined when the Luminance has decayed to less than 50% of the initial Luminance

### EXTERNAL DIMENSIONS

Customer No. :

PIN DESCRIPTION			
1	VSS	18	/RD
2	SW	19	DO
3	VDD2	20	D1
4	FB	21	D2
5	SENSE	22	D3
6	VBREF	23	D4
7	NC	24	D5
8	NC	25	D6
9	NC	26	D7
10	VDD1	27	IREF
11	CB6	28	VCOMH
12	P/S	29	VPP
13	NC		
14	/CS		
15	/RES		
16	A0		
17	/WR		

detail: "A"/scale 10X

**NOTES:**

- GENERAL TOLERANCE:  $\pm 0.2$
- OPERATING TEMPERATURE:  $-20^{\circ}\text{C}$  TO  $70^{\circ}\text{C}$
- STORAGE TEMPERATURE:  $-30^{\circ}\text{C}$  TO  $80^{\circ}\text{C}$
- DRIVE IC : SH1101A
- FPC: TOD9M0028FPC-A1-E
- ROHS COMPLIANCY

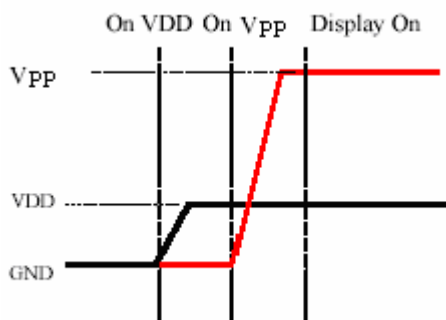
CUSTOMER APPROVE		AMEND		NO.	NAME	QTY	Part No.
Mechanical	Electrical	△		1/64	DUTY		MULTI-INNO TECHNOLOGY CO.,LTD.
		△	MODIFY TOTAL THICKNESS	20080711	/	BIAS	PRODUCT NO. MI12864JO
		△	加上ROHS COMPLIANCY标注	20071022	/	Vpp	DRN 杨学宇 20080711 DSN 杨学宇 20080711
		△	去除PC上LED驱动电路标注	20070918	/	VDD	CHRD 谢志生 20080711 APPD 苏君海 20080711
		NO.	CONTENT	DATE	/	0' CLOCK	NOT IN SCALE UNIT mm SHEET: 3/3

### TIMING OF POWER SUPPLY

To Protect OLED panel and extend the panel life time, the driver IC power up/down routine should include a delay period between high voltage and low voltage power sources turn on/off.

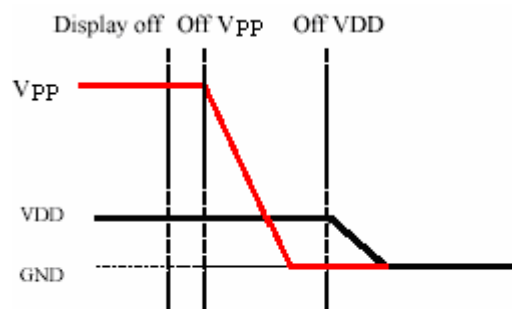
Power up Sequence:

1. Power up  $V_{DD}$
2. Delay 100ms
3. Power up  $V_{PP}$  (High Voltage)
4. Delay 100ms
5. Send Display ON command



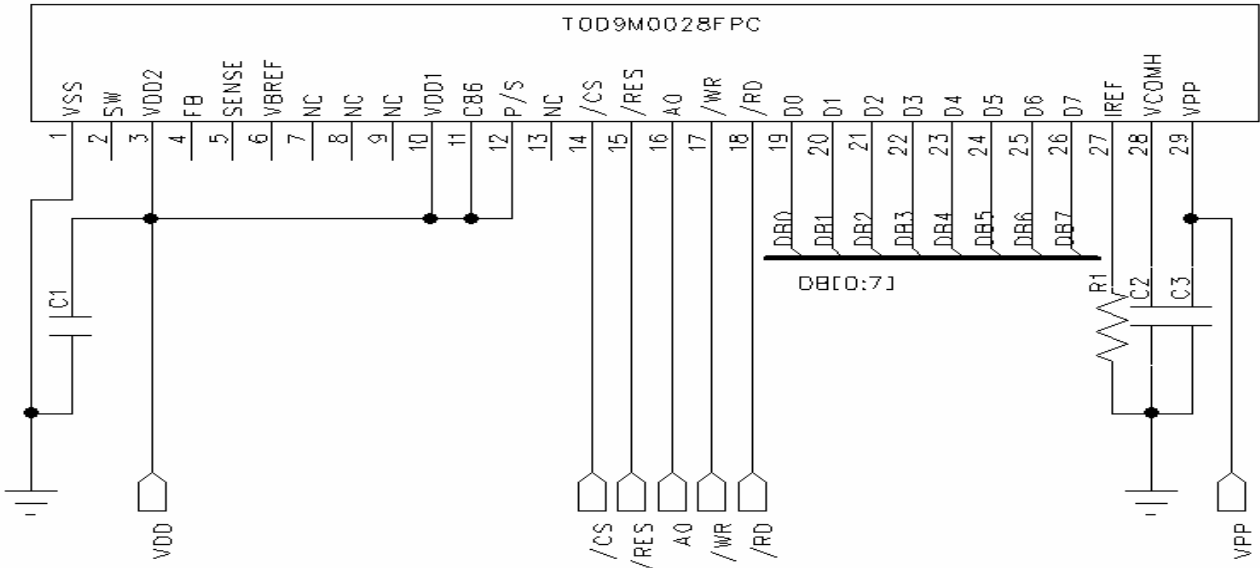
Power down Sequence:

1. Send Display OFF command
2. Power down  $V_{PP}$ (High Voltage)
3. Delay 100ms
4. Power down  $V_{DD}$

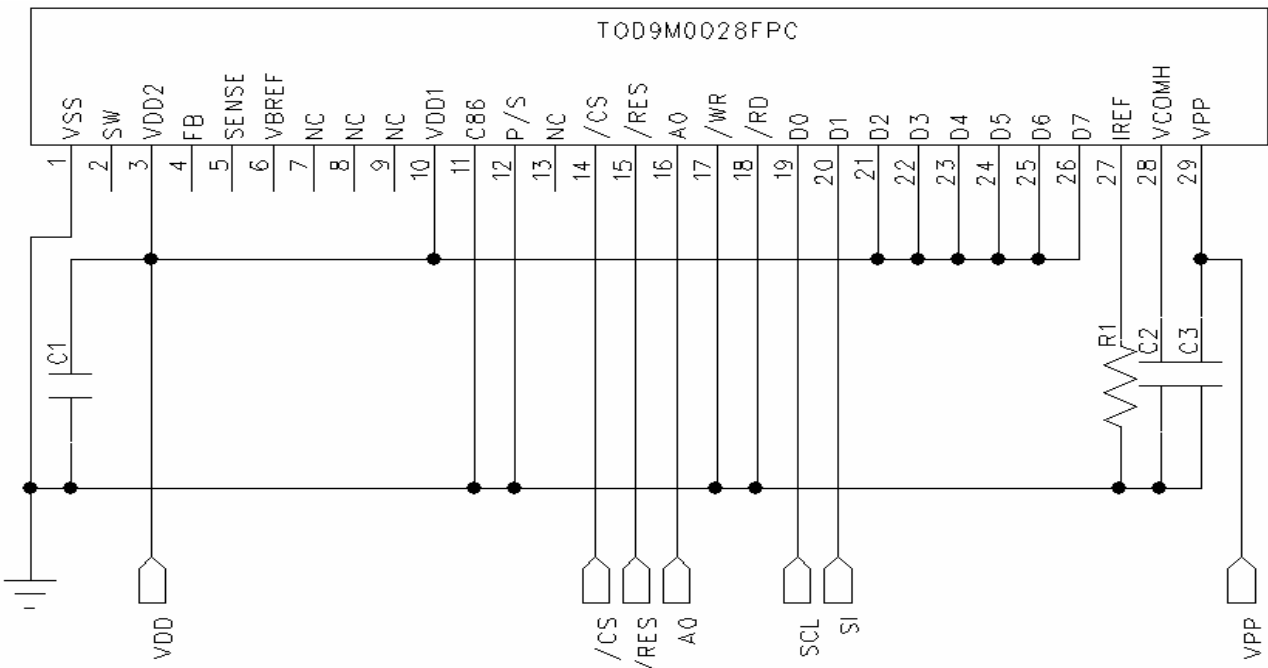


■ SCHEMATIC EXAMPLE

◆ 8080 Series Interface Application Circuit(External VPP):



◆ Serial Interface Application Circuit(External VPP):



NOTE:

1. R1=910KΩ,C1=C2=C3=4.7uF

2.In Serial interface mode , read function is not possible.