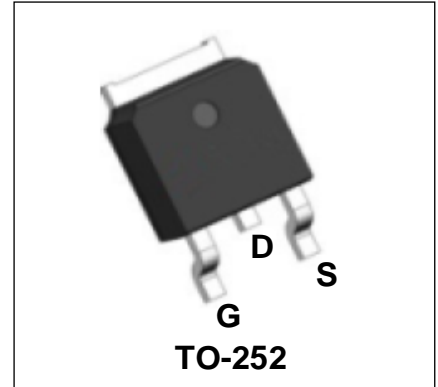


250V N-Channel Enhancement Mode Power MOSFET

Description

WMO15N25T2 uses advanced power trench technology that has been especially tailored to minimize the on-state resistance and yet maintain superior switching performance.



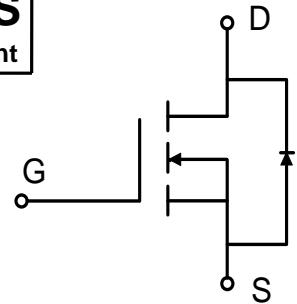
Features

- $V_{DS} = 250V$, $I_D = 15A$
 $R_{DS(on)} < 238m\Omega$ @ $V_{GS} = 10V$
 $R_{DS(on)} < 270m\Omega$ @ $V_{GS} = 4.5V$
- High Speed Power Smooth Switching, Logic Level
- Lead Free
- 100% EAS Guaranteed



Applications

- Synchronous Rectification in SMPS
- Motor Backlighting
- DC/DC Converters



Absolute Maximum Ratings (T_A = 25°C, unless otherwise noted)

Parameter		Symbol	Value	Unit
Drain-Source voltage		V_{DS}	250	V
Gate-Source voltage		V_{GS}	±20	V
Continuous Drain Current	T _C =25°C	I_D	15	A
	T _C =100°C		9.5	
Pulsed Drain Current ¹		I_{DM}	60	A
Single Pulse Avalanche Energy ²		EAS	3.5	mJ
Total Power Dissipation	T _C =25°C	P_D	78.1	W
Operating Junction and Storage Temperature Range		T_J, T_{STG}	-55 to 150	°C

Thermal Characteristics

Parameter	Symbol	Value	Unit
Thermal Resistance from Junction-to-Ambient ³	$R_{\theta JA}$	61	°C/W
Thermal Resistance from Junction-to-Case	$R_{\theta JC}$	1.6	°C/W

Electrical Characteristics ($T_J = 25^\circ\text{C}$, unless otherwise noted)

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 250\mu A$	250	-	-	V
Gate-body Leakage current	I_{GSS}	$V_{DS} = 0V, V_{GS} = \pm 20V$	-	-	± 100	nA
Zero Gate Voltage Drain Current	$T_J=25^\circ\text{C}$	$V_{DS} = 250V, V_{GS} = 0V$	-	-	1	μA
	$T_J=100^\circ\text{C}$		-	-	100	
Gate-Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\mu A$	1	2	3	V
Drain-Source on-Resistance ⁴	$R_{DS(on)}$	$V_{GS} = 10V, I_D = 5A$	-	185	238	m Ω
		$V_{GS} = 4.5V, I_D = 3A$	-	195	270	
Forward Transconductance ⁴	g_{fs}	$V_{DS} = 5V, I_D = 20A$	-	10	-	S
Dynamic Characteristics⁵						
Input Capacitance	C_{iss}	$V_{DS} = 125V, V_{GS} = 0V, f = 1MHz$	-	570	-	pF
Output Capacitance	C_{oss}		-	31	-	
Reverse Transfer Capacitance	C_{rss}		-	5.5	-	
Gate Resistance	R_G	$f = 1MHz$	-	4.2	-	Ω
Switching Characteristics⁵						
Gate Resistance	R_G	$V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$	-	4.2	-	Ω
Total Gate Charge	Q_g	$V_{GS} = 10V, V_{DS} = 125V, I_D = 5A$	-	11.2	-	nC
Gate-Source Charge	Q_{gs}		-	1.9	-	
Gate-Drain Charge	Q_{gd}		-	4	-	
Turn-on Delay Time	$t_{d(on)}$	$V_{GS} = 10V, V_{DD} = 125V, R_G = 10\Omega, I_D = 5A$	-	9.5	-	ns
Rise Time	t_r		-	5.8	-	
Turn-off Delay Time	$t_{d(off)}$		-	14.5	-	
Fall Time	t_f		-	4.8	-	
Body Diode Reverse Recovery Time	t_{rr}	$I_F = 5A, di/dt = 100A/\mu s$	-	39	-	ns
Body Diode Reverse Recovery Charge	Q_{rr}		-	80	-	nC
Drain-Source Body Diode Characteristics						
Diode Forward Voltage ⁴	V_{SD}	$I_S = 1A, V_{GS} = 0V$	-	-	1.2	V
Continuous Source Current	I_S	$T_C = 25^\circ\text{C}$	-	-	15	A

Notes:

1. Repetitive rating, pulse width limited by junction temperature $T_{J(MAX)} = 150^\circ\text{C}$
2. The EAS data shows Max. rating . The test condition is $V_{DD} = 25V, V_{GS} = 10V, L = 0.4mH, I_{AS} = 3.2A$
3. The data tested by surface mounted on a 1 inch2 FR-4 board with 2OZ copper, The value in any given application depends on the user's specific board design.
4. The data tested by pulsed , pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$.
5. This value is guaranteed by design hence it is not included in the production test..

Typical Characteristics

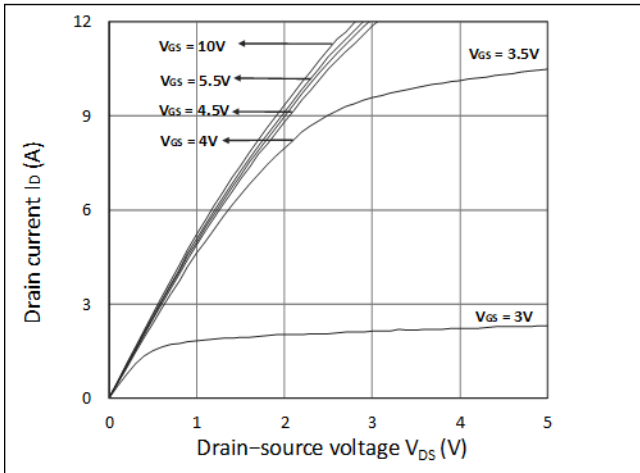


Figure 1. Output Characteristics

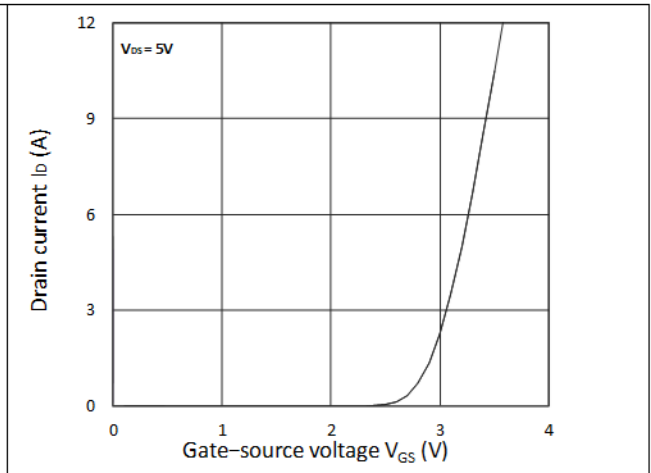


Figure 2. Transfer Characteristics

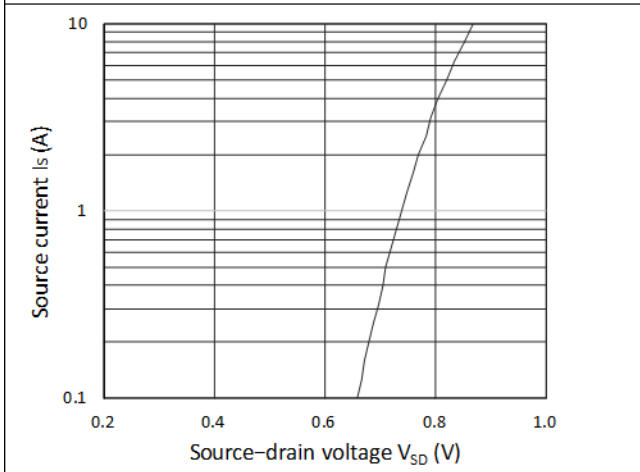


Figure 3. Forward Characteristics of Reverse

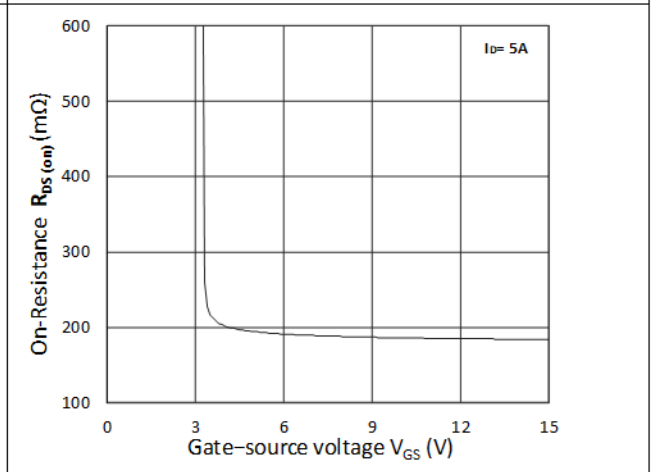


Figure 4. $R_{DS(ON)}$ vs. V_{GS}

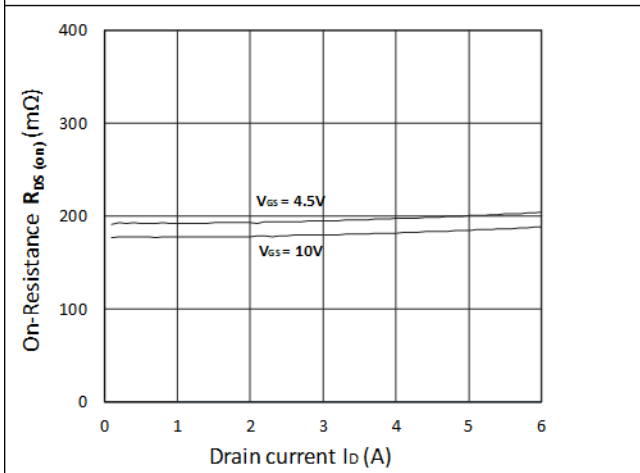


Figure 5. $R_{DS(ON)}$ vs. I_D

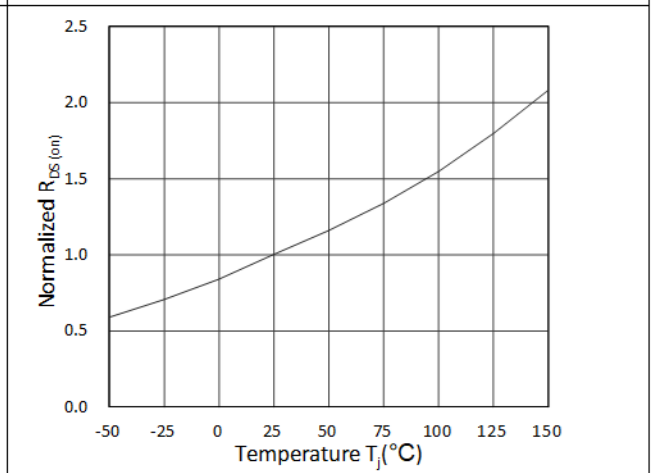


Figure 6. Normalized $R_{DS(ON)}$ vs. Temperature

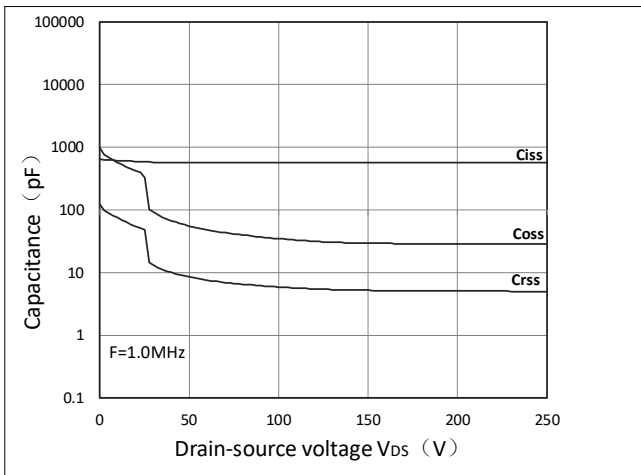


Figure 7. Capacitance Characteristics

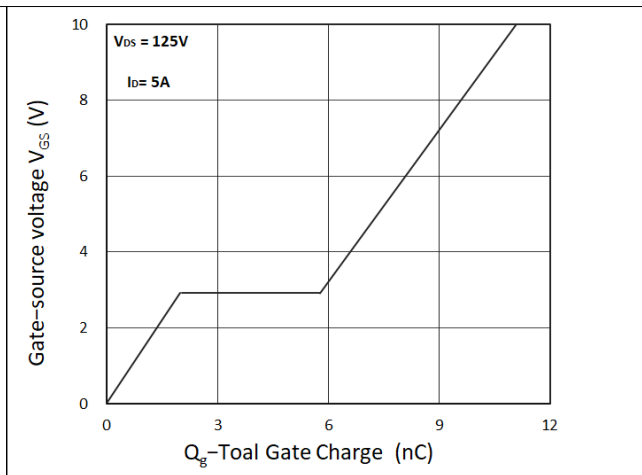


Figure 8. Gate Charge Characteristics

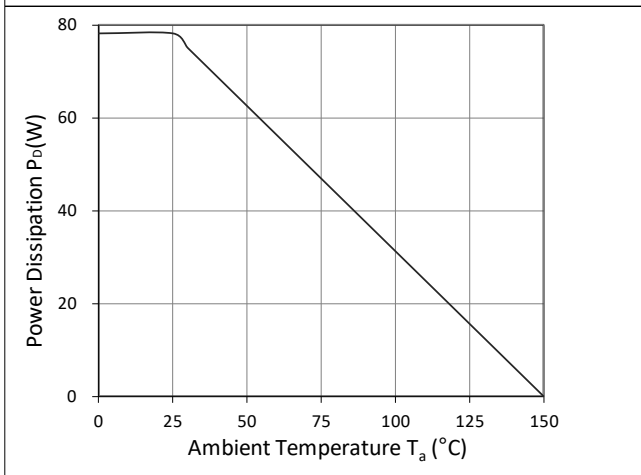


Figure 9. Power Dissipation

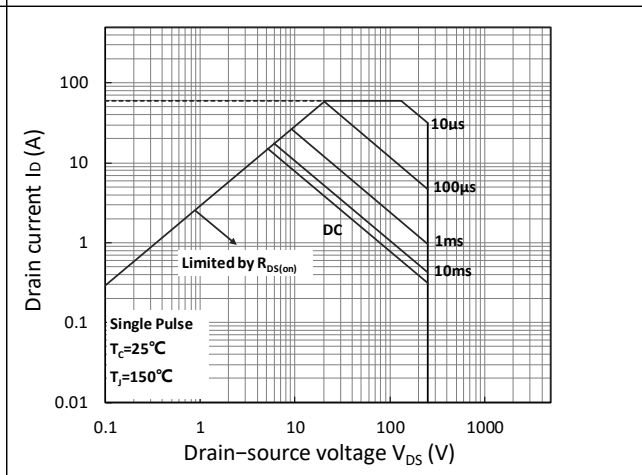


Figure 10. Safe Operating Area

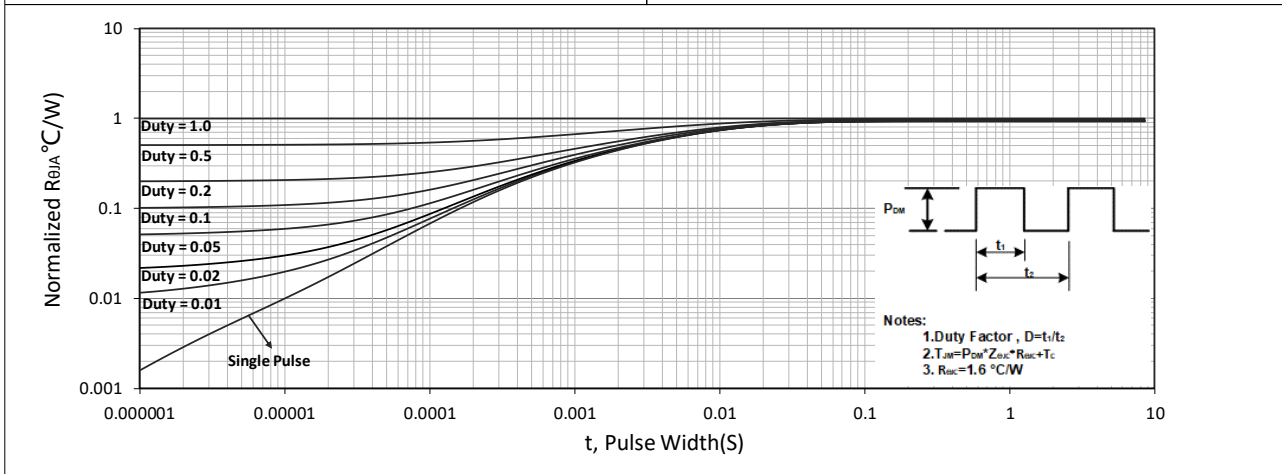


Figure 11. Normalized Maximum Transient Thermal Impedance

Test Circuit

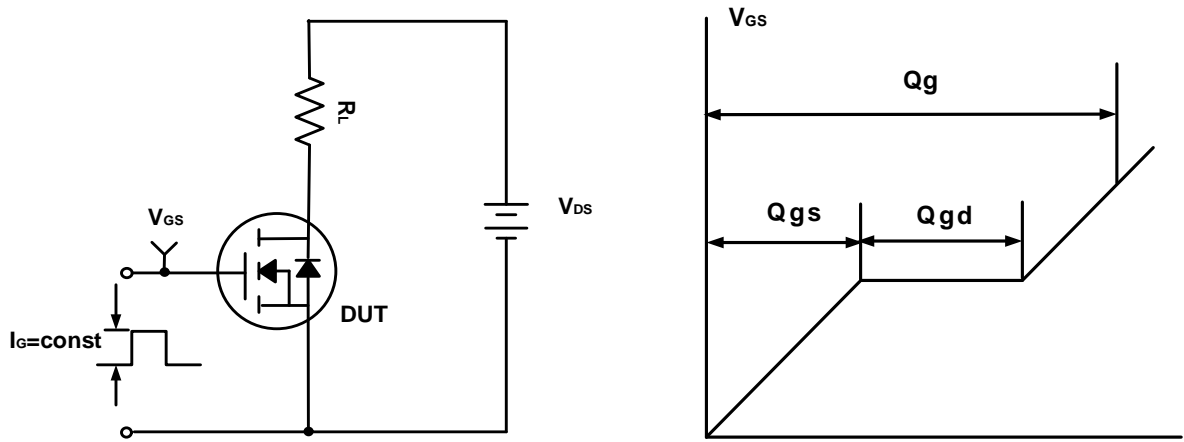


Figure A. Gate Charge Test Circuit & Waveforms

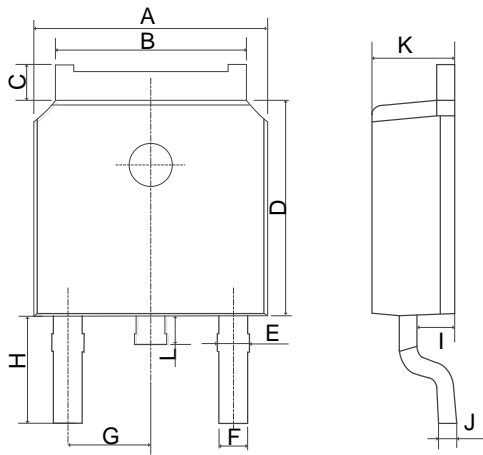


Figure B. Switching Test Circuit & Waveforms



Figure C. Unclamped Inductive Switching Circuit & Waveforms

Mechanical Dimensions for TO-252



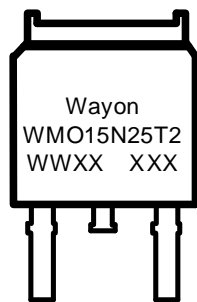
COMMON DIMENSIONS

SYMBOL	MM	
	MIN	MAX
A	6.40	6.80
B	5.13	5.50
C	0.88	1.28
D	5.90	6.22
E	0.68	1.10
F	0.68	0.91
G	2.29REF	
H	2.90REF	
I	0.85	1.17
J	0.51REF	
K	2.10	2.50
L	0.40	1.00

Ordering Information

Part	Package	Marking	Packing method
WMO15N25T2	TO-252	WMO15N25T2	Tape and Reel

Marking Information



WMO15N25T2 = Device code

WWXX XXX= Date code

Contact Information

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WAYON website: <http://www.way-on.com>

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